

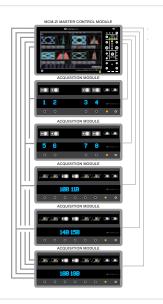
# **UNIQUELY POWERFUL**



# More channels, faster analysis

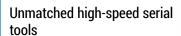
The most powerful high bandwidth oscilloscope available powers through complex calculations faster:

- Up to 80 channels using ChannelSync™ architecture
- Powerful 24-core server-class CPU
- Up to 192 GB of RAM available





# Best for Serial Data & DDR



- Simple and powerful compliance test automation
- Most complete serial data analysis toolset
- Fastest DDR Test Journey

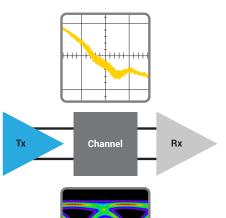




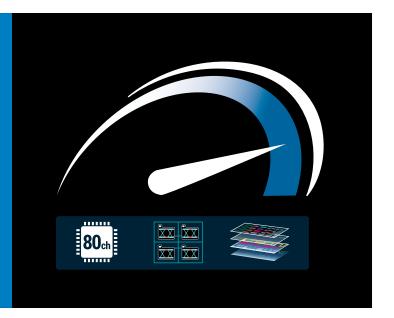
# Analyze the Whole Link at Once

# End-to-end link signal integrity analysis

- Import S-parameter files from WavePulser® 40iX and other instruments
- De-embed fixtures, emulate channels, apply transmitter/receiver equalization
- Debug with CrossSync™ PHY protocol and electrical cross-layer analysis







# High Bandwidth, Uniquely Powerful



**LabMaster 10 Zi-A** 

# MORE OSCILLOSCOPE CHANNELS, FASTER ANALYSIS



LabMaster 10 Zi-A oscilloscopes can be configured with more channels than any other oscilloscope (up to 80 at up to 36 GHz, or up to 40 at up to 65 GHz). The Master Control Module contains a powerful server-class CPU configurable with up to 192 GB of RAM to more quickly perform the most complex calculations.

### **Up to 80 Oscilloscope Channels**

LabMaster is the only high-bandwidth oscilloscope with the unique ChannelSync architecture for precise timing synchronization by design - channel to channel jitter is only 130 fs. LabMaster greatly simplifies greater than 4 channel oscilloscope setups and provides acquisition confidence.

# Powerful 24-core Server-class CPU

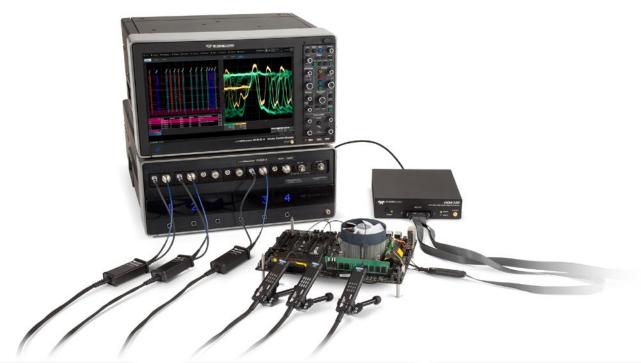
LabMaster has the most powerful CPU in any oscilloscope - an Intel® Xeon® Gold 6240R 24-core server-class CPU (or better). LabMaster plows quickly and easily through complex calculations, such as PCI Express® receiver calibration routines. Serial data jitter and eye diagram analysis goes faster with LabMaster.

### **Up to 192 GB of RAM Available**

LabMaster's powerful CPU is augmented with an enormous amount of available system RAM to ensure that long memory calculations are handled with ease.

# BEST OSCILLOSCOPE FOR SERIAL DATA AND DDR

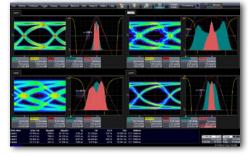
The LabMaster 10 Zi-A is the best oscilloscope platform for high-speed serial data and advanced memory compliance and debug. The combination of more channels, mixed-signal options, serial decoders, and comprehensive compliance, analysis and debug software options puts you in the drivers seat.





# Simple and Powerful Compliance Test Automation

- QualiPHY® automation software supports PCI Express®, USB®, HDMI®, DisplayPort™, DDR, and many other serial data standards
- Fully automated transmitter and receiver testing and fastest receiver test calibration
- Step-by-step instructions and automatic report generation
- Automated pass/fail test reports



### Most Complete Serial Data Analysis Toolset

- Multi-lane jitter and eye analysis
- LaneScape<sup>™</sup> comparison modes
- Vertical noise and crosstalk analysis
- NRZ and PAM support
- Integrated equalization, emulation and de-embedding
- Virtual probing



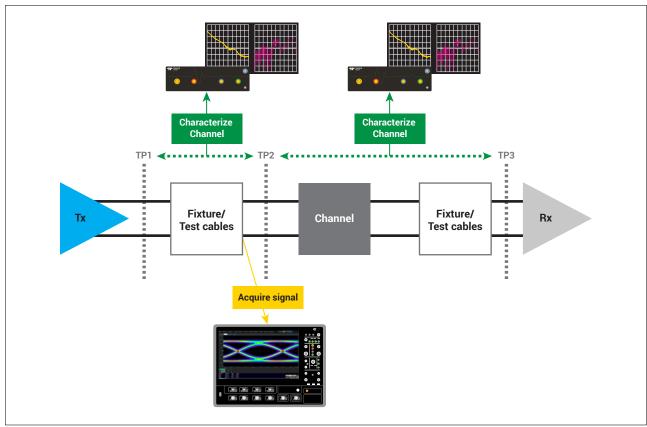
### **Fastest DDR Test Journey**

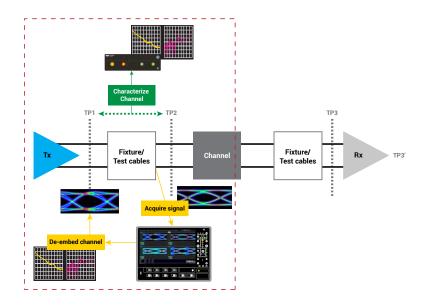
- Maximize turn-on through validation
- Accelerate pre-compliance and finetuning
- QualiPHY automated compliance testing
- DDR5 software bundle (QualiPHY & Debug)
- Multi-scenario Views eye, mask, measurements in each view
- External MSO "HDA125" decode/ trigger and R/W separation

# ANALYZE THE WHOLE LINK

Combining the WavePulser® 40iX High-speed Interconnect Analyzer, LabMaster 10 Zi-A oscilloscope and SDAIII-CompleteLinQ option gives the most complete signal integrity analysis toolkit available. Quickly characterize the entire signal path from transmitter to receiver, acquire high-fidelity waveforms at a convenient test point, and then easily analyze the signal at any point of interest.

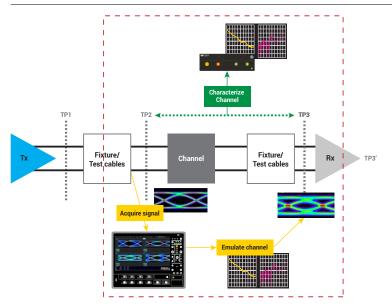






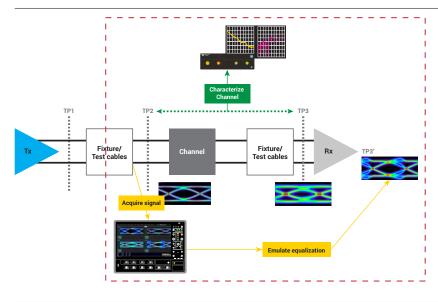
### De-embed fixtures and test cables

- Measure S-parameter models using WavePulser 40iX, or import from other measurements or simulation tools
- Sophisticated Eye Doctor and VirtualProbe tools easily and accurately remove effects of fixtures and cables from acquired oscilloscope waveforms
- Apply the full SDAIII-CompeteLinQ toolkit to de-embedded waveforms for full eye, jitter and noise analysis directly at the output pins of the device under test



### **Emulate real-world channel losses**

- WavePulser 40iX simplifies and speeds up accurate measurements of test channel loss profiles
- Channel model s-parameter files can be easily imported from the WavePulser 40iX or elsewhere into Eye Doctor and VirtualProbe tools in the oscilloscope
- Acquire waveforms at any point in the signal path, then use VirtualProbe to cleanly embed the effects of the channel
- Use the full analysis capability of SDAIII-CompleteLinQ to compare eye, jitter and noise measurements at multiple test points simultaneously



# **Emulate transmitter and receiver equalization**

- SDAIII-CompleteLinQ with Eye Doctor enables the emulation of all common equalization types, including:
  - Transmitter emphasis
  - Receiver FFE
  - Receiver CTLE
  - Receiver DFE

# SDAIII-COMPLETELINQ

The SDAIII software option provides the most comprehensive jitter decomposition, eye diagram and analysis tools with advanced signal integrity tools for emulation, de-embedding and equalization simulation.



### **Key Attributes**

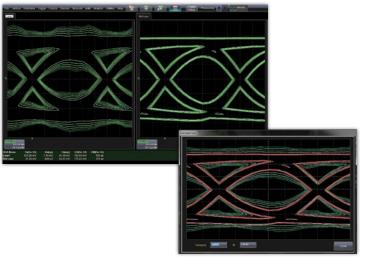
- Eye diagram (a), eye mask failure
   (b) and IsoBER eye opening analysis (c)
- Jitter spectrum (a) with noise floor display (b) and inverse FFT of the periodic jitter (c)
- Data dependent jitter (DDj) plot for each bit in synch with pattern (a) and with histogram (b)
- **4.** Time interval error (TIE) jitter track analysis
- 5. Jitter histograms (a) with bathtub curves (b) and CDF plot (c)
- **6.** Intersymbol interference (ISI) plots pinpoint bit sequences that have high ISI and are sources of bit errors
- Jitter measurements table with full details for one or more "lanes" plus reference

### **Advanced Signal Integrity Tools**

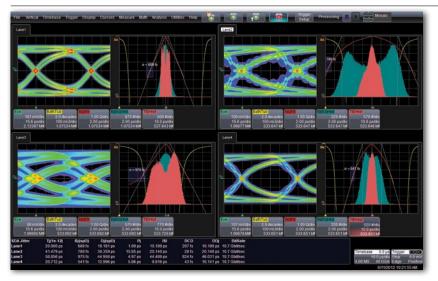
Complete set of tools for: channel emulation; fixture, cable or channel de-embedding/embedding; adding or removing emphasis; performing CTLE, FFE or DFE equalization.



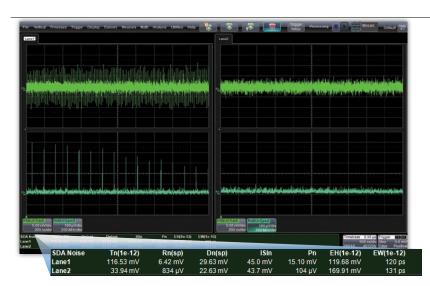
# **COMPREHENSIVE SERIAL DATA ANALYSIS**



Use the unique crosstalk eye to view and compare noise in a way that cannot be done with a traditional eye diagram.



A comprehensive set of jitter measurements, extrapolations and decompositions, with associated views for complete understanding, provides the best capability to debug problems faster.



View noise measurements in both time and frequency domains for insight into sources of crosstalk leading to bit errors.

### **Fast Single or Multiple Eye Diagrams**

- Up to four real-time and one reference comparison eye diagram, NRZ or PAM
- Single lane with multiple-point or multiconfiguration analysis
- Analyze multiple lanes simultaneously
- Fast eye diagram creation
- Reference lane simplifies multi-scenario testing
- IsoBER displays expected eye infringement to a user-settable bit error rate (BER)
- Crosstalk eye contour plots display the impact of excessive noise

### Comprehensive Jitter Decomposition & Analysis

- Complete Tj, Rj and Dj decomposition numerics on up to four lanes/configurations plus a reference, NRZ or PAM
- Three different jitter decomposition models
- Complete random (Rj) and non-data dependent jitter (Rj+BUj) parameters and views
- Comprehensive data dependent jitter (DDj) analysis, including DDj plots and histograms, digital pattern display, and ISI plot by pattern
- Periodic jitter (Pj) inverse FFT
- Other jitter parameters including bounded uncorrelated jitter (BUj) and odd-even jitter (OEj)

### **Vertical Noise & Crosstalk Analysis**

- Tools for complete aggressor/victim analysis
- Measure, extrapolate and decompose vertical noise just as you do with (horizontal) jitter
- Noise tracks, histograms and spectrums providing deep insight into noise sources
- Crosstalk eye contour plot shows probabilistic extent of noise, both inside and outside the eye

# **FASTEST DDR TEST JOURNEY**

The DDR test journey can be quick when the right tools for engineers enable smooth transitions from all stages of design, DDR turn-on, initial validation testing to fine-tuning, optimizing, pre-compliance and final compliance. Accelerate testing by confidently testing designs quickly and easily. Teledyne LeCroy covers BGA testing tools for JEDEC standards such as DDR2/3/3L/4/5 and LPDDR2/3/4/4X.



### 1. Interposers and Probes

- Interposers from reliable partners (Nexus and EyeKnowHow)
- High bandwidth probes with solder-in tips

### 2. External MSO - HDA125 "Logic Analyzer"

- Market's only trigger & decode up to DDR5
- Validate 20+ Command Address packets
- Highest accuracy for Read/Write separation

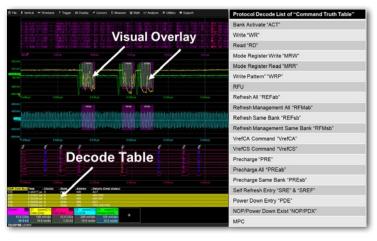
### 3. Tools for Turn-on Through Pre-compliance

- Multi-scenario viewing fast-tracks testing
- Exclusive toolkit with JEDEC defined measurements
- Eliminate signal quality errors with virtual probing

### 4. Automated DDR5 Compliance Testing

- Measure the latest JEDEC specification at the BGA
- Increase repeatability & test consistency
- Save Pass/Fail reports with annotated screenshots

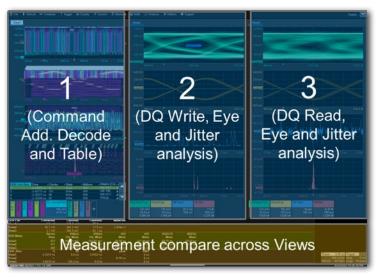
# **DDR5 BUNDLE INCLUDES QUALIPHY & DEBUG TOOLKIT**



### **Decode and Trigger**

Establishing basic operation, signal checks and responses is the foundation of board turn-ons. Decode the command bus to understand if it's correctly communicating and know if Read and Write packets are present.

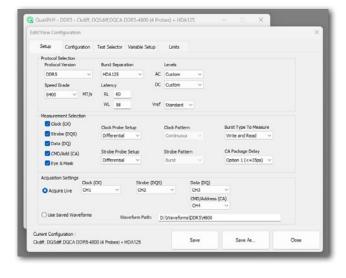
- Industry's only decode & trigger up to DDR5
- Decode 20+ packets from JEDEC Command Truth Table
- Perform better R/W separation using the command bus
- Overlay Read/Write visuals on channels
- Trigger on six different bus packet types



### **Multiple Scenario Viewing Areas**

DDR stability occurs when the DRAM has been fine-tuned and optimized. This occurs when the voltage and timing parameters have been adjusted and measured for peak performance.

- Fast-track tuning stages with unique comparisons
- Interactively perform eye diagram, mask and JEDEC specific measurements on each view
- High-speed external MSO (HDA125) enables the highest accuracy for Read & Write packet separation.



### **Automated Compliance Testing**

QualiPHY automated compliance testing enables faster test times by reducing inconsistencies, testing to the JEDEC standard and allows users to quickly stop and root-cause failures with the DDR Debug Toolkit.

- DDR5 JEDEC measurements for DQ, DQS, CK, CA signals
- Supports System Level testing at the BGA
- Save Pass/Fail reports with annotated screenshots.
- Analyze compliance failures in a dedicated Debug Toolkit

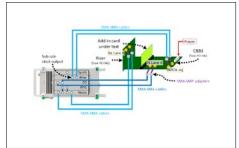
# **COMPLETE PCI EXPRESS® ELECTRICAL TEST SOLUTIONS**

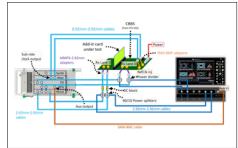
Teledyne LeCroy's PCI Express electrical test solutions combine superior instruments with sophisticated jitter, eye diagram, debug and compliance software for all versions of PCI Express.

- Automated Transmitter, Receiver and Link Equalization (LEQ) testing with QualiPHY software options
- Visibility from physical layer through protocol operations
- LabMaster 10 Zi-A is gold suite certified for all relevant PCI Express
   5.0 tests
- LabMaster 10 Zi-A supports
   PCI Express 6.0 test with PAM4 capabilities









### **Transmitter (Tx) Testing**

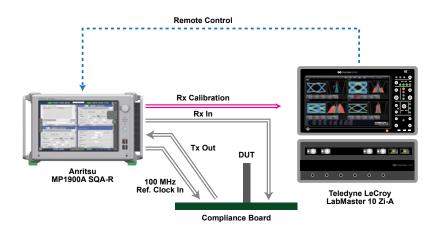
- Base specification and compliance testing for add-in cards and systems in CEM, M.2 and U.2 form factors
- QualiPHY fully automates collection and processing of transmitter waveforms
- Supports TF-PCIE4-CTRL controller for full fixture and DUT automation
- Debug electrical compliance issues faster with SDAIII-CompleteLinQ software

### **Receiver (Rx) Testing**

- Receiver calibration and testing using the LabMaster and Anritsu MP1900A BERT
- QualiPHY controls both the LabMaster and MP1900A
- Use WavePulser 40iX for receiver channel characterization and calibration
- Single QualiPHY user interface for Tx and Rx testing

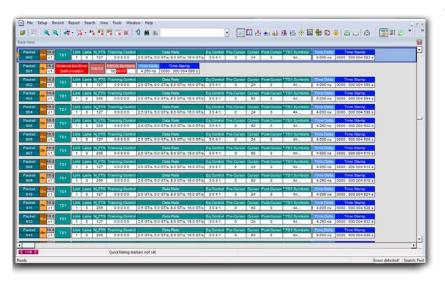
### **Link Equalization (LEQ) Testing**

- Fully automated Tx and Rx LEQ testing using QualiPHY with SigTest integration
- Test, fixture and DUT automation for fast throughput without lots of manual steps
- Go directly from compliance test to cross-layer debug using ProtoSync on the LabMaster and LTSSM analysis on the MP1900A
- Link the LabMaster with a protocol analyzer using CrossSync<sup>™</sup> PHY for even deeper interoperability debug



### **Superior PCle® Test Solutions**

- Approved PCI-SIG® gold suite solution for PCIe electrical compliance test programs
- High accuracy and repeatability due to superior signal quality
- Fastest receiver test calibration
- Complete DUT and fixture automation



# Visibility from Physical Layer Through Protocol Operations

- LTSSM logging and state-machine triggering
- ProtoSync integrates industry-standard protocol display and physical-layer analysis
- Go directly from Link Equalization compliance tests to deep debug

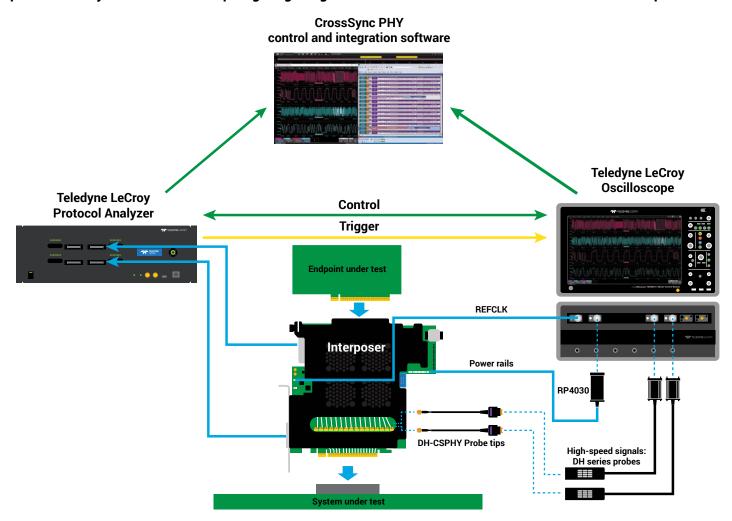


# Comprehensive PCI Express 6.0 Characterization Tools

- Unique transmitter equalization tools
- Highest confidence jitter measurements with PAM4 eye diagrams
- Most complete SNDR analysis

# CrossSync\*PHY FOR PCI EXPRESS®

Interoperability issues can lead to finger-pointing exercises that cost money and time-to-market. Teledyne LeCroy CrossSync PHY software and interposers merge the functions of your Teledyne LeCroy PCI Express protocol analyzer and oscilloscope - giving insight into link behavior that no other instrument can provide.



# Validate and debug active link operation

- CrossSync PHY capable interposers enable observation of both electrical and protocol behavior without disturbing the link
- Sideband signals, reference clock and power rails are all easily accessible to oscilloscope probes
- Optional high-bandwidth oscilloscope probing points for PCI Express data lanes

# Quickly resolve interoperability issues by capturing the entire protocol stack

- Trigger protocol analyzer and oscilloscope captures on the same high-level event
- Easily measure timing relationships between protocol and electrical domains
- Faster root-cause analysis means fewer costly finger-pointing exercises

# Analyze link training with integrated physical and protocol views

- Observe electrical-level results of protocol-level commands
- Combined navigation means always knowing which protocol and electrical behaviors happen at the same time
- No single instrument can deliver this level of cross-layer insight into link training behavior

The CrossSync PHY software option for your Teledyne LeCroy oscilloscope enables precise, intuitive navigation between time-correlated protocol analyzer and oscilloscope traces.

Oscilloscope timebase and protocol analyzer acquisition window remain synchronized while navigating through the combined acquisition, for total confidence in timing behavior.

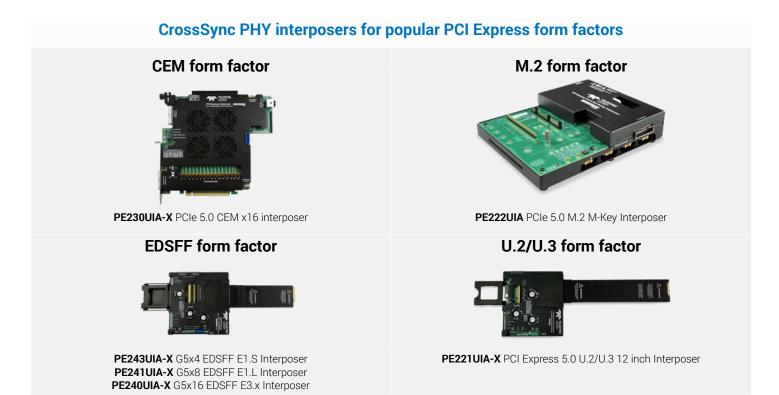


CrossSync PHY capability enhances Teledyne LeCroy's industry-leading set of PCI Express protocol analysis interposers by adding high-fidelity oscilloscope probing points with simple and convenient signal access.

### Easily probe and observe:

- High-speed data signals
- Reference clock behavior

- Power rail voltage and current
- Sideband signals

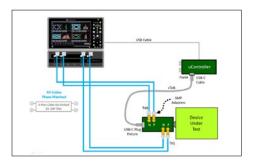


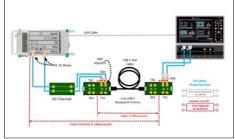
# USB AND USB TYPE-C® ELECTRICAL TEST SOLUTIONS

In 2011, Teledyne LeCroy became the first USB-IF approved "Gold Suite" for USB 3.0 at 5 Gb/s. Today, the USB Type-C connector carries multiple lanes each up to 40 Gb/s data supporting USB4® Version 2.0, USB4, USB 3.2, Thunderbolt™ 3/4 and DisplayPort™ 2 standards. Teledyne LeCroy continues to be the trusted leader with:

- USB-IF approved "Gold Suite" PHY Tx/Rx compliance testing
- VESA approved DisplayPort over USB Type-C compliance testing
- Up to 4 lanes (8 channels) of simultaneous acquistion
- The deepest signal integrity toolbox
- Unmatched PHY-logic and USB Type-C sideband debug









### **Fastest PHY Compliance**

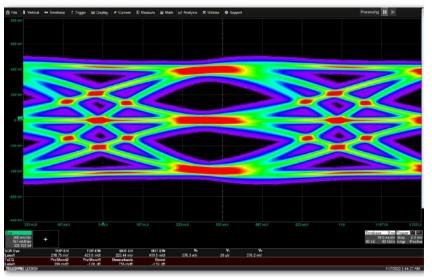
- QualiPHY software automates all USB-C standard Transmitter (Tx) and Receiver (Rx) compliance tests using a single, friendly user interface
- Accurate, repeatable Rx testing with Anritsu MP1900A BERT
- Support for both USB-IF and 3rd party fixtures and software tools
- Single QualiPHY user interface for Tx and Rx testing

### Deepest SI Toolbox

- Choose either USB-IF SigTest or Teledyne LeCroy SDAIII analysis methodology
- Debug electrical compliance issues faster with SDAIII-CompleteLinQ eye diagrams, jitter and noise analysis software
- Characterize USB4 Version 2.0 PAM3 eye diagrams and Tx Equalization with SDAIII-PAMx software
- WavePulser 40iX simplifies and speeds up receiver channel characterization and calibration

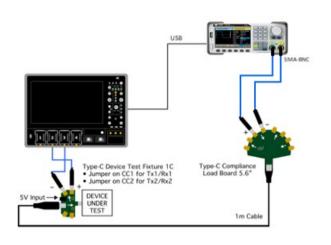
### **PHY-logic & Sideband Debug**

- See the whole link with CrossSync PHY for USB4 and Thunderbolt
- USB4 and USB 3.2/2.0 serial decode options provide decode of USB packets with graphical, intuitive, color-coded decode overlays
- ProtoSync integrates industrystandard protocol display
- USB-PD (Power Delivery) TDMP and DisplayPort-AUX DMP provide unmatched visibility of USB Type-C sideband signals for system debug



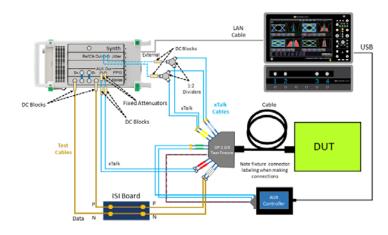
### **USB4 and Thunderbolt 3/4**

- QPHY-USB4-TX-RX provides automated transmitter compliance test automation per the USB4 Gen2 (10 Gb/s NRZ), Gen3 (20 Gb/s NRZ), and Version 2.0 (40 Gb/s PAM3); and Thunderbolt Gen2 (10.3125 Gb/s NRZ) and Gen3 (20.625 Gb/s NRZ) electrical Compliance Test Specifications (CTS)
- Integrates USB4 ETT for DUT control with the Wilder-Tech USB4 test controller, and Thunderbolt electrical scripts with Thunderbolt 3 controllers
- Fully automates receiver calibration and test with the Anritsu MP1900A high-speed BERT



### **USB 3.2**

- QPHY-USB3.2-TX-RX fully automates the USB 3.2 Tx and Rx CTS for Gen1 (5 Gb/s) and Gen2 (10 Gb/s), LFPS Tx/Rx and SCD/LBPM tests
- Supports a variety of generators for Tx compliance pattern control including Teledyne Test Tools AFG, Wilder-Tech USB Type-C controllers and Anritsu MP1900A BERT
- Fully automates Rx calibration and test with the Anritsu MP1900A high-speed BERT solution

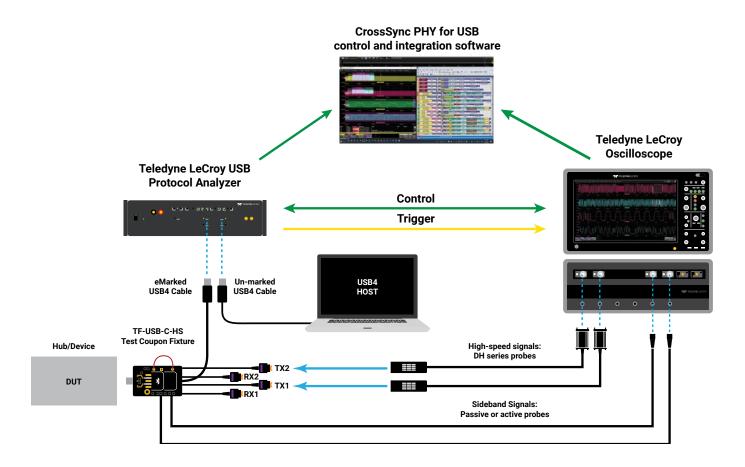


### **DisplayPort over USB Type-C**

- QPHY-DP2-SOURCE software automates source (Tx) testing for all DisplayPort 2 (UHBR20, UHBR13, UHBR10) and 1.4a (HBR3, HBR2, HBR, RBR) data rates up to four lanes
- QPHY-DP2-SINK software automates
   DisplayPort 2 and 1.4a sink (Rx) calibration
   and testing with the Anritsu MP1900A high-speed BERT solution
- DPAUX DMP provides AUX channel decode, serial data measurements and physical layer measurements
- Supports all VESA approved test fixtures including Standard/Enhanced DP, mDP and USB Type-C

# CrossSync<sup>®</sup>PHY FOR USB4<sup>®</sup> AND THUNDERBOLT<sup>™</sup>

Interoperability issues can lead to finger-pointing exercises that cost money and time-to-market. Teledyne LeCroy CrossSync PHY software merges the functions of your Teledyne LeCroy protocol analyzer and oscilloscope - giving insight into link behavior that no other instrument can provide.



# Validate and debug active link operation

- TF-USB-C-HS Test Coupon Fixtures enable observation of both electrical and protocol behavior without disturbing the link
- USB Type-C Sideband signals are all accessible using passive or active probes
- High-bandwidth oscilloscope probing points for USB data lanes

# Quickly resolve interoperability issues by capturing the entire protocol stack

- Trigger protocol analyzer and oscilloscope captures on the same high-level event
- Easily measure timing relationships between protocol and electrical domains
- Faster root-cause analysis means fewer costly finger-pointing exercises

# Analyze link training with integrated physical and protocol views

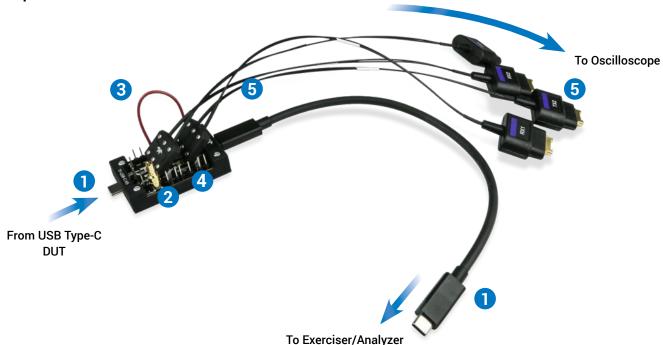
- Observe electrical-level results of protocol-level commands
- Combined navigation means always knowing which protocol and electrical behaviors happen at the same time
- No other solution can deliver this level of cross-layer insight into link training

The CrossSync PHY software option for your Teledyne LeCroy oscilloscope enables precise, intuitive navigation between time-correlated protocol analyzer and oscilloscope traces.

Oscilloscope timebase and protocol analyzer acquisition window remain synchronized while navigating through the combined acquisition, for total confidence in timing behavior.



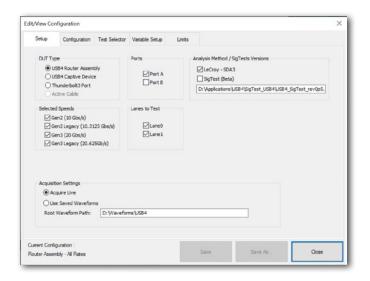
CrossSync PHY capability enhances Teledyne LeCroy's industry-leading Protocol Analyzer/Exercisers by adding high-fidelity oscilloscope probing points with simple and convenient signal access using USB Type-C Test Coupon Fixtures.



- Transparent signal path through the test coupon fixture's USB-C Plug, Receptacle, and included 0.3 meter USB Type-C cable
- 2. Vbus access using Active Single Ended or Voltage Rail Probe
- Current loop for measuring Vbus current through the test coupon fixture
- 4. Access SBU1/SBU2 (USB4 Sidebands and DP-AUX), CC1/ CC2 (Power Delivery), and D+/D-(USB1.1/2.0) signals using square pins
- 5. High-speed TX1/TX2 and RX1/RX2 signals captured using a permanently attached DH-SI Series probe tips

# QUALIPHY AUTOMATED SOFTWARE TEST FRAMEWORK

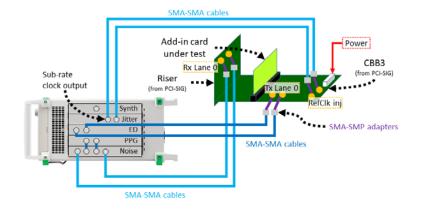
QualiPHY is Teledyne LeCroy's automated software test framework for performing standardized tests on high-speed serial interfaces. QualiPHY automation software is available for PCI Express, USB, DDR, DisplayPort, HDMI and other technologies - for a full list, see our Oscilloscope Features, Options, and Accessories catalog.



### **Simplified Setup**

QualiPHY dialogs help the user configure all aspects of test execution, including:

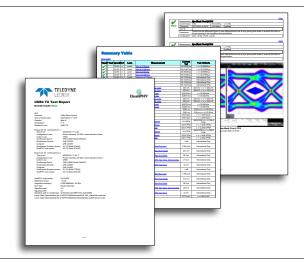
- Selecting the set of tests to run
- Configuring test parameters
- Customizing limits
- Options to stop after each test or execute sequentially



### **Streamlined Test Execution**

QualiPHY guides the user though connection and execution of each test, resulting in increased repeatability.

- Clear, informative connection diagrams help simplify complex test setups and reduce mistakes
- Dialogs explain test execution and required Device Under Test (DUT) settings
- Simple, powerful Host Program Control interface enables complete automation of QualiPHY with external scripting environments (for selected QualiPHY products)



### **Informative Reporting**

QualiPHY produces comprehensive reports documenting test results.

- Save reports in PDF or HTML format
- Screenshots and tabular results included
- Summary table at the start of the report makes it easy to tell pass/fail results at a glance

# HIGH BANDWIDTH DIFFERENTIAL PROBES

The DH series of 8 to 30 GHz active differential probes provides high input dynamic range, large offset capability, low loading and excellent signal fidelity with a range of connection options.

### **General Purpose Probing up to 30 GHz**

Teledyne LeCroy's DH series of 8 GHz to 30 GHz differential probes offer the combination of bandwidth, input range and offset capability to address any high-speed probing requirement - from debugging serial data interfaces to validating DDR memory systems.

### **Exceptional Signal Fidelity**

DH series probes provide superior loading characteristics and are calibrated with a custom "fine-tuned" frequency response. The ultra-low loading and flat frequency response ensure accurate measurements.

### **Wide Variety of Tips**

Two 30 GHz solder-in leads let you choose between a 3.5 Vpp input range for general-purpose applications, or high sensitivity with exceptionally low noise. Also available are a 1-meter long 16 GHz high-temperature tip, a 16 GHz handheld browser tip and an 8 GHz QuickLink adapter for connecting mixed-signal probe tips.



### **Tip Identification**

Each DH series tip has its own data onboard - the oscilloscope software automatically selects the correct tip type and precisely corrects for its effects. The result is superior signal fidelity and superior ease-of-use.

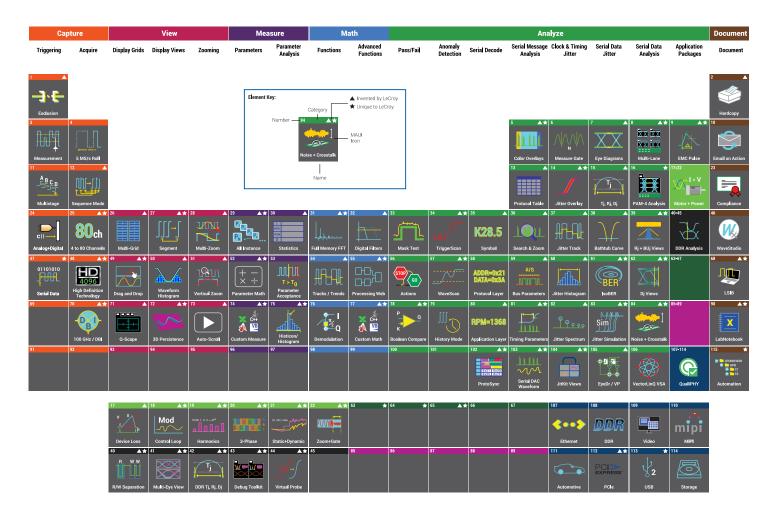
### **Digital Logic Probing Options**

### **HDA125 High-speed Digital Analyzer**

The HDA125 turns your Teledyne LeCroy oscilloscope into the highest-performance, most flexible mixed-signal solution with 12.5 GS/s digital sampling rate (3 GHz digital clock rate) on 18 input channels and the QuickLink probing solution. Ideal for validation of DDR interfaces.



# POWERFUL, DEEP TOOLBOX



### Our heritage

Teledyne LeCroy's 50+ year heritage is in processing long records to extract meaningful insight. We invented the digital oscilloscope and many of the additional waveshape analysis tools.

### **Our obsession**

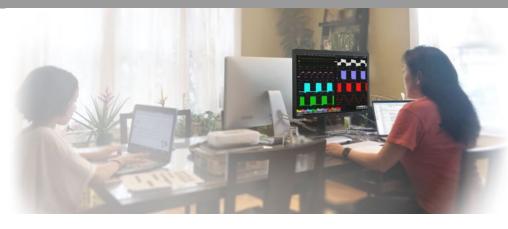
Our tools and operating philosophy are standardized across much of our product line. This deep toolbox inspires insight; and your moment of insight is our reward.

### **Our invitation**

Our Periodic Table of Oscilloscope Tools explains the toolsets that Teledyne LeCroy has deployed in our oscilloscopes. Visit our interactive website to learn more about them.

teledynelecroy.com/tools

# MAUI STUDIO - WORKS WHERE YOU ARE



Unleash the power of a
Teledyne LeCroy oscilloscope
anywhere, using a PC with MAUI
Studio Pro. Work remotely from
your oscilloscope and collaborate
with ease.

### Flexibility to Work Anywhere

MAUI Studio provides the flexibility to remotely work anywhere, and allows anyone anywhere to execute real-time analysis by connecting to an oscilloscope through an Ethernet connection or by analyzing a saved LabNotebook.

### **Collaborate with Ease**

Using MAUI Studio, you can share a LabNotebook file saved from an oscilloscope with all of your colleagues, and everyone will have access to the same software options that are found on your oscilloscope.

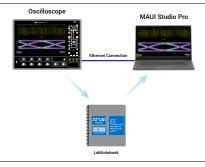
### The Power of MAUI Studio

Get all the unbelievable analytical capabilities of your oscilloscope on your PC. MAUI Studio has all the analysis tools needed to analyze complex waveform data, enabling your lab's oscilloscopes to be freed up for other activities.



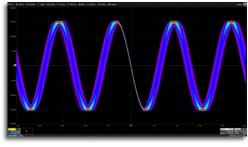
### **Remote Connection**

- Connect to an oscilloscope through an Ethernet connection
- Transfer waveforms and setups from an oscilloscope to MAUI Studio Pro
- Transfer setups from MAUI Studio Pro to an oscilloscope
- Import software options by establishing a remote connection to an oscilloscope



### **Offline Analysis**

- Recall a LabNotebook file to analyze saved waveforms, measurements and setups
- Import software options by recalling a LabNotebook file
- Have access to the same software found on your oscilloscope



### **Arbitrary Function Generator**

- Generate advance waveforms using the AFG
- Easily generate a PAM4 signal
- Add jitter to a clock signal to simulate real-world signal integrity impairments

LabMaster

	Labiylaster 10-20Zi-A	Labiyiaster 10-25Zi-A	Labiviaster 10-30Zi-A	Labwaster 10-36Zi-A		
Vertical System						
•						
Analog Bandwidth @ 50 Ω (-3 dB) (2.4/2.92mm Inputs)	20 GHz (≥5 mV/div)	25 GHz (≥5 mV/div)	30 GHz (≥5 mV/div)	36 GHz (≥5 mV/div)		
Rise Time (10–90%, 50 $\Omega$ - test limit)	19.3 ps (test limit, flatness mode)	15.4 ps (test limit, flatness mode)	12.8 ps (test limit, flatness mode)	10.7 ps (test limit, flatness mode)		
Rise Time (20–80%, 50 <b>Ω</b> - typical)	14.5 ps (flatness mode)	11.6 ps (flatness mode)	9.6 ps (flatness mode)	8.0 ps (flatness mode)		
Input Channels	Up to 80, depending on conf	iguration selected. (Any comb	pination of up to 80 2.92mm i	nput channels)		
Vertical Resolution	8 bits; up to 11 bits with enh					
Effective Number of Bits (ENOB)*	5.32	5.16	5.03	4.90		
Vertical Noise Floor (rms, typical, 50 Ω)		0.44	0.40	0.56		
5 mV/div	0.38 mV	0.44 mV	0.49 mV	0.56 mV		
10 mV/div	0.38 mV	0.44 mV	0.49 mV	0.56 mV		
20 mV/div	0.64 mV	0.74 mV	0.81 mV	0.92 mV		
50 mV/div	1.40 mV	1.60 mV	1.70 mV	1.88 mV		
100 mV/div	3.38 mV	3.88 mV	4.28 mV	4.83 mV		
200 mV/div	6.10 mV	6.98 mV	7.53 mV	8.30 mV		
500 mV/div	14.00 mV	16.00 mV	17.00 mV	18.25 mV		
* Average value across bandwith, input signal &	87.5% of full scale					
Sensitivity		nV/div, fully variable (5-9.9 m	V/div via zoom)			
ocholivity	<b>66 iz (2.32mm).</b> 6 mm 6 6 6 6	niv/div, rully variable (0 3.5 m	v, arv via 200mj			
DC Vertical Gain Accuracy	±1% E.S. (typical), offset at 0	V; ±1.5% F.S. (test limit), offse	et at 0V			
(Gain Component of DC Accuracy)	= · · · · · · (•) p · · · · · // • · · · · · · ·	, =				
Channel-Channel	<b>DC to 36 GHz</b> : 60 dB (>1000	n·1)				
			(alice a attingual to uniqual)			
Isolation	(For any two 2.92mm input of	channels, same or different v/	div settings, typicai)			
011						
Offset Range	50 Ω:					
	±500 mV @ 5-75 mV/div	u				
	±4 V @ 76 mV/div -500mV/d	IIV				
DC Vertical Offset Accuracy	±(1.5% of offset setting + 1.5% F.S. + 1 mV) (test limit)					
ŕ	`	, , ,				
Vertical System	0.00 mans Immedia 10 V/mas v	76 ma) //ali. / E E) /mma a @ . 76 ma)	1/45.			
Maximum Input Voltage	2.92 mm Inputs: ±2 Vmax@	<76mV/div, 5.5Vrms@≥76mV	//div			
Input Coupling	<b>2.92 mm Inputs:</b> 50 Ω: DC, G	ND				
Input Impedance	<b>2.92mm Inputs:</b> 50 Ω+/-2%					
Pandwidth Limitars	Eully variable from 1 CLI-+-	inatrument handwidth in in-	omente of 100 MI Iz			
Bandwidth Limiters		instrument bandwidth in incre				
Rescaling	<b>Length:</b> meters, inches, feet,	yards, miles; Mass: grams, sli	ugs; <b>Temperature:</b> celsius, fal	renheit, kelvin; <b>Angle:</b>		
		ec, cycles, revolutions, turns; \				
	s2, in/s2, ft/s2, g0; Volume: I	iters, cubic meters, cubic inch	nes, cubic feet, cubic yards; <b>Fc</b>	rce (Weight): newton, grain,		
	ounce, pound; Pressure: pas	cal, bar, atmosphere (technica	al), atmosphere (standard), to	rr, psi; <b>Electrical</b> : volts, amps		
		peres reactive, farad, coulomb				
		Magnetic: weber, tesla, henry				
	Rotating Machine: radian/se	econd, frequency, revolution/s	econd, revolution/minute Non	n. lb-ft. lb-in oz-in watt		
	horsepower; Other. %.	seema, mequemey, revenument, e		.,		
Horizontal - Analog Channels	<sub>1</sub> ,,					
Timebases	Internal timehase with 10 CI	Hz clock frequency common t	to all innut channels. Single			
THIODUSCS	distributed 10 GHz clock for	all channels ensures precise	synchronization with timing			
	accuracy between all channel	els identical to that provided v	within a single conventional			
	oscilloscope package.	alo lacintidal to triat provided v	manir a single, conventional			
Time/Division Range		um capture time is based on	minimum sample rate of			
•	200kS/s and installed memo	ory).	•			
Clock Accuracy	< 0.1 ppm + (aging of 0.05pp	om/yr from last calibration)				

LabMaster

LabMaster

LabMaster

	LabMaster 10-50Zi-A	LabMaster 10-59Zi-A	LabMaster 10-65Zi-A
Vertical System			
Analog Bandwidth @ 50 Ω (-3 dB) (1.85mm Inputs)	50 GHz (≥10 mV/div)	59 GHz (≥10 mV/div)	65 GHz (≥10 mV/div)
nalog Bandwidth @ 50 Ω (-3 dB) 2.4/2.92mm Inputs)	36 GHz (≥5 mV/div)	36 GHz (≥5 mV/div)	36 GHz (≥5 mV/div)
Rise Time (10–90%, 50 $\Omega$ - test limit)	8 ps (test limit, flatness mode)	6.9 ps (test limit, flatness mode)	6.5 ps (test limit, flatness mode)
Rise Time (20–80%, 50 <b>Ω</b> - typical)	6 ps (flatness mode)	5.2 ps (flatness mode)	4.9 ps (flatness mode)
nput Channels	Up to 40, depending on configuration Up to 80 @ 36 GHz	selected.	
ertical Resolution	8 bits; up to 11 bits with enhanced res	solution (ERES)	
Effective Number of Bits (ENOB)*	4.79	4.67	4.60
Vertical Noise Floor (rms, 50 Ω)			
	0.89 mV	0.95 mV	0.97 mV
10 mV/div	0.891117	0.951117	0.57 1117
10 mV/div 20 mV/div	1.48 mV	1.58 mV	1.61 mV
10 mV/div 20 mV/div 50 mV/div	1.48 mV 3.20 mV	1.58 mV 3.45 mV	1.61 mV 3.55 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div	1.48 mV 3.20 mV 5.05 mV	1.58 mV	1.61 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signal Sensitivity	1.48 mV 3.20 mV 5.05 mV	1.58 mV 3.45 mV 5.6 mV  lly variable (5-9.9 mV/div via zoom) lly variable. use of external attenuators.	1.61 mV 3.55 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signs  Sensitivity  DC Vertical Gain Accuracy Gain Component of DC Accuracy)	1.48 mV 3.20 mV 5.05 mV al 87.5% of full scale  50 Ω (2.92mm): 5 mV-500mV/div, fu 50 Ω (1.85mm): 10 mV-80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%	1.58 mV 3.45 mV 5.6 mV  lly variable (5-9.9 mV/div via zoom) lly variable. use of external attenuators.	1.61 mV 3.55 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signs  Gensitivity  OC Vertical Gain Accuracy Gain Component of DC Accuracy)  Channel-Channel	1.48 mV 3.20 mV 5.05 mV al 87.5% of full scale  50 Ω (2.92mm): 5 mV – 500mV/div, fu 50 Ω (1.85mm): 10 mV – 80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%  DC to 36 GHz: 60 dB (>1000:1)	1.58 mV 3.45 mV 5.6 mV  lly variable (5-9.9 mV/div via zoom) lly variable. use of external attenuators. F.S. (test limit), offset at 0 V	1.61 mV 3.55 mV 5.75 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signs  Gensitivity  OC Vertical Gain Accuracy Gain Component of DC Accuracy)  Channel-Channel	1.48 mV 3.20 mV 5.05 mV al 87.5% of full scale  50 Ω (2.92mm): 5 mV-500mV/div, fu 50 Ω (1.85mm): 10 mV-80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%  DC to 36 GHz: 60 dB (>1000:1) (For any two 2.92mm input channels,	1.58 mV 3.45 mV 5.6 mV  lly variable (5-9.9 mV/div via zoom) lly variable. use of external attenuators.	1.61 mV 3.55 mV 5.75 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signs ensitivity  C Vertical Gain Accuracy Gain Component of DC Accuracy) channel-Channel	1.48 mV 3.20 mV 5.05 mV 5.05 mV  al 87.5% of full scale  50 Ω (2.92mm): 5 mV – 500mV/div, fu 50 Ω (1.85mm): 10 mV – 80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%  DC to 36 GHz: 60 dB (>1000:1) (For any two 2.92mm input channels, 36 to 65 GHz: 40 dB (>100:1)	1.58 mV 3.45 mV 5.6 mV  Illy variable (5-9.9 mV/div via zoom) Illy variable. use of external attenuators. F.S. (test limit), offset at 0 V  same or different v/div settings, typical	1.61 mV 3.55 mV 5.75 mV
10 mV/div 20 mV/div 50 mV/div 80 mV/div Average value across bandwith, input signs  Gensitivity  DC Vertical Gain Accuracy Gain Component of DC Accuracy) Channel-Channel Solation	1.48 mV 3.20 mV 5.05 mV 5.05 mV  al 87.5% of full scale  50 Ω (2.92mm): 5 mV – 500mV/div, fu 50 Ω (1.85mm): 10 mV – 80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%  DC to 36 GHz: 60 dB (>1000:1) (For any two 2.92mm input channels, 36 to 65 GHz: 40 dB (>100:1)	1.58 mV 3.45 mV 5.6 mV  lly variable (5-9.9 mV/div via zoom) lly variable. use of external attenuators. F.S. (test limit), offset at 0 V	1.61 mV 3.55 mV 5.75 mV
10 mV/div 20 mV/div 50 mV/div	1.48 mV 3.20 mV 5.05 mV 5.05 mV  al 87.5% of full scale  50 Ω (2.92mm): 5 mV – 500mV/div, fu 50 Ω (1.85mm): 10 mV – 80mV/div, fu Higher gain settings possible through ±1% F.S. (typical), offset at 0 V; ±1.5%  DC to 36 GHz: 60 dB (>1000:1) (For any two 2.92mm input channels, 36 to 65 GHz: 40 dB (>100:1)	1.58 mV 3.45 mV 5.6 mV  Illy variable (5-9.9 mV/div via zoom) Illy variable. use of external attenuators. F.S. (test limit), offset at 0 V  same or different v/div settings, typical	1.61 mV 3.55 mV 5.75 mV

DC Vertical Offset Accuracy	±(1.5% of offset setting + 1.5% F.S. + 1 mV) (test limit)			
Vertical System				
Maximum Input Voltage	2.92 mm Inputs: ±2 Vmax@<76mV/div, 5.5Vrms@≥76mV/div			
	1.85 mm Inputs: ±2 Vmax@≤80mV/div			
Input Coupling	<b>2.92 mm Inputs:</b> 50 Ω: DC, GND			
	<b>1.85 mm Inputs:</b> 50 Ω: DC			
Input Impedance	<b>2.92mm Inputs:</b> 50 Ω+/-2%			
	<b>1.85mm Inputs:</b> 50 Ω+/-2%			
Bandwidth Limiters	Fully variable from 1 GHz to instrument bandwidth in increments of 100 MHz			
Rescaling	Length: meters, inches, feet, yards, miles; Mass: grams, slugs; Temperature: celsius, fahrenheit, kelvin; Angle: radian, arcdegr, arcmin, arcsec, cycles, revolutions, turns; Velocity: m/s, in/s, ft/s, yd/s, miles/s; Acceleration: m/s2, in/s2, ft/s2, g0; Volume: liters, cubic meters, cubic inches, cubic feet, cubic yards; Force (Weight): newton, grain, ounce, pound; Pressure: pascal, bar, atmosphere (technical), atmosphere (standard), torr, psi; Electrical: volts, amps, watts, volt-amperes, volt-amperes reactive, farad, coulomb, ohm, siemen, volt/meter, coulomb/m2, farad/meter, siemen/meter, power factor; Magnetic: weber, tesla, henry, amp/meter, henry/meter; Energy: joule, Btu, calorie; Rotating Machine: radian/second, frequency, revolution/second, revolution/minute, N·m, lb-ft, lb-in, oz-in, watt, horsepower; Other: %.			
Horizontal - Analog Channels				
Timebases	Internal timebase with 10 GHz clock frequency common to all input channels. Single, distributed 10 GHz clock for all channels ensures precise synchronization with timing accuracy between all channels identical to that provided within a single, conventional oscilloscope package.			
Time/Division Range	For >36 GHz Mode: 10 ps/div - 320 µs/div (maximum capture time is based on 160 GS/s and installed memory). For ≤36 GHz Mode: 10 ps/div-256 s/div (maximum capture time is based on minimum sample rate of 200kS/s and installed memory).			
Clock Accuracy	< 0.1 ppm + (aging of 0.05 ppm/yr from last calibration)			

	LabM 10-20		LabMaster 10-25Zi-A	LabMaster 10-30Zi-A	LabMaster 10-36Zi-A
Horizontal - Analog Channels (cont'd)					
Sample Clock Jitter	Up to 3.2ms A 50fsrms (Inter 50fsrms (Exter Up to 6.4ms A 130fsrms (Inter	cquired Time Ranal Timebase Ranal Timebase Ranal Timebase Recquired Time Ranal Timebase Remal Timebase	eference) deference) ange: Reference)		
Delta Time Measurement Accuracy	$\sqrt{2} * \sqrt{\frac{No.}{Slew}}$	ise \2 + (Sample	Clock Jitter) <sup>2</sup> (RMS) + (clock ac	curacy * reading) (seconds)	
Jitter Measurement Floor	$\sqrt{\frac{Not}{Slew}}$	+ (Sample	Clock Jitter) <sup>2</sup> (RMS, seconds, T	IE)	
Jitter Between Channels	<250fsrms (TIE, typical, m	easured at max	imum bandwidth)		
Channel-Channel Deskew Range			max., each channel		
External Timebase Reference (Input)	10 MHz or 100	MHz; 50 Ω impe	edance, applied at the rear	input of MCM-Zi-A Master Cor	ntrol Module
External Timebase Reference (Output)	10 MHz or 100	) MHz ; 50 Ω imp	pedance, output at the rear	of MCM-Zi-A Master Control N	Module
Assumination Apples Channels					
Acquisition - Analog Channels Sample Rate (Single-Shot)	80 GS/s on ea	sh channel			
Sample Rate (Single-Shot)	80 G5/S 011 ea	cri criaririei.			
Standard Memory	32 Mpts, 1,000	) segments			
Memory Options			Max		
	Option	Mem/Ch	Segments		
	S-32	32 Mpts	7,500		
	M-64	64 Mpts	15,000		
	L-128	128 Mpts	15,000		
	VL-256	256 Mpts	15,000		
	XL-512	512 Mpts	15,000		
Intersegment time	1 μs				
Averaging (FDF0)			n sweeps; continuous avera	aging to 1 million sweeps	
Enhanced Resolution (ERES) Envelope (Extrema)		bits vertical res	o 1 million sweeps		
Interpolation	Linear or Sin x		o i million sweeps		
·			105 10 0/0/0		
Vertical, Horizontal, Acquisition  Maximum Input Frequency	- Digital Chanr 3 GHz	ieis with HDA	1Z3-18-81NU		
Minimum Detectable Pulse Width	167ps				
Input Dynamic Range		ingle ended inpu	ıt		
pac by narmo name	±7.5V max diff				
Input Impedance (Flying Leads)		kΩ, 0.12pF diffe	erential		
Input Channels	18 Digital Char		-		
Maximum Input Voltage		ngle ended inpu	ıt		
	±15V max diffe				
Minimum Input Voltage Swing	150 mV p-p				
Threshold Selections	User defined				
Threshold Accuracy		of threshold sett			
User Defined Threshold Range		er channel in 5 r			
User Defined Hysteresis Range		/ settable per ch	annel		
Sample Rate	12.5 GS/s				
Channel-to-Channel Skew	±160ps				
Deskew Range	±1.6ns in 80ps	sieps			

	LabMaste 10-50Zi-A		LabMaster 10-59Zi-A	LabMaster 10-65Zi-A
Horizontal - Analog Channels (co	nt'd)			
Sample Clock Jitter	Up to 3.2ms Acquired T 50fsrms (Internal Timeb 50fsrms (External Timeb Up to 6.4ms Acquired T 130fsrms (Internal Timeb 130fsrms (External Timeb	ase Reference) base Reference) ime Range: base Reference	)	
Delta Time Measurement Accuracy	□ / Noise \2		-1 r) <sup>2</sup> (RMS) + (clock accuracy * reading) (secon	nds)
Jitter Measurement Floor	$\sqrt{\left(\frac{Noise}{SlewRate}\right)^2 + ($	Sample Clock Jitter	r)² (RMS, seconds, TIE)	
Jitter Between Channels	<190fsrms (TIE, typical, measured bandwidth	at maximum (	<150fsrms (TIE, typical, measured at maximum bandwidth)	<130fsrms (TIE, typical, measured at maximum bandwidth)
Channel-Channel Deskew Range	±9 x time/div. setting, or			
External Timebase Reference (Input)			oplied at the rear input of MCM-Zi-A N	
External Timebase Reference (Output)	10 MHz or 100 MHz ; 50	$\Omega$ impedance, $\alpha$	output at the rear of MCM-Zi-A Maste	r Control Module
Association Analog Channels				
Acquisition - Analog Channels Sample Rate (Single-Shot)	160 GS/s on each channe 80 GS/s on each channe			
Standard Memory	64 Mpts, 1,000 segment		340.	
Managary Ontions				
Memory Options	Option Mem/C	Max h Segmen	ato.	
	•	_	its	
	S-32 64 Mpt			
	M-64 128 Mp	s 7,500		
	L-128 256 Mp	s 15,000		
	VL-256 512 Mp	s 15,000	)	
	XL-512 1024 Mg			
			ecification for LabMaster 10 Zi-A 36 Gl	Hz Systems)
Intersegment time	1 μs	crice memory op	Compation to Easimaster to 217100 of	- Le dysterris)
Averaging		million eweens:	continuous averaging to 1 million sw	Jeans
Enhanced Resolution (ERES)	From 8.5 to 11 bits verti		Continuous averaging to 1 million sw	леерз
Envelope (Extrema)	Envelope, floor, or roof fo		n sweens	
Interpolation	Linear or Sin x/x	or up to i illilloi	Томесро	
merpolation	Linear or only x			
Vertical, Horizontal, Acquisition -	Digital Channels with	HDA125-18-9	SYNC	
Maximum Input Frequency	3 GHz	115/1120 10		_
Minimum Detectable Pulse Width	167ps			
Input Dynamic Range	±10V on any single ende	d input		
, ,	±7.5V max differential	,		
Input Impedance (Flying Leads)	QL-SI tips: 110 kΩ, 0.12p	F differential		
Input Channels	18 Digital Channels			
Maximum Input Voltage	±15V on any single ende	d input		
	±15V max differential			
Minimum Input Voltage Swing	150 mV p-p			
Threshold Selections	User defined			
Threshold Accuracy	$\pm$ (25mV + 3% of thresho			
User Defined Threshold Range	±5V, settable per channe			
User Defined Hysteresis Range	50mV - 600mV settable	per channel		
Sample Rate	12.5 GS/s			
Channel-to-Channel Skew	±160ps			
Deskew Range	±1.6ns in 80ps steps			

	LabMaster 10-20Zi-A	LabMaster 10-25Zi-A	LabMaster 10-30Zi-A	LabMaster 10-36Zi-A
Triggering System				
Modes	Normal, Auto, Single and Sto	pp		
Sources	Any Ch 1-4 (Edge, Window, S only) on additional 10-xxZi-A Slope and level unique to eac	MART, Cascade triggers), AUX Acquisition Modules (Channe ch source except line trigger.	i, internal Fast Edge; or any inpo ls 5 and higher).	ut channel (Edge trigger
Coupling	DC, AC, HFRej, LFRej			
Pre-trigger Delay		adjustable in 1% increments o		
Post-trigger Delay		<u>me mode, limited at slower tin</u>	ne/div settings	
Hold-off	From 2 ns up to 20 s or from			
Trigger and Interpolator Jitter		e assisted), 2 ps rms (typical,	hardware)	
Internal Trigger Level Range	±4.1 div from center		410	
External Trigger Level Range	(Only Ch1-4 Acquisition Mod	A Acquisition Module: Aux (±0. Jule has ""active" AUX Input)	4 V)	
Maximum Trigger Rate		nd (in Sequence Mode, up to 4	- channels)	
Trigger Sensitivity with Edge Trigger	For Ch 1-80 of a LabMaster	10 Zi-A system:		
(1.85/2.4/2.92mm Inputs)	3 div @ < 12 GHz			
	1.5 div @ < 3 GHz 1.0 div @ < 200 MHz			
	(for DC coupling, ≥ 10 mV/di	v 50 0 )		
Trigger Sensitivity with Edge Trigger	For Ch 1-4 LabMaster 10xx-			
(Aux Input)	2 div @ < 1 GHz.			
	1.5 div @ < 500 MHz,			
	1.0 div @ < 200 MHz, (for DC coupling)			
Max. Trigger Frequency,		aster 10xx-Zi-A Acquisition M	odrile.	
SMART Trigger	2.0 GHz @ ≥ 10 mV/div	doter Toxx 217 (7 toquiottion 14)	oddie.	
- 39-	(minimum triggerable width	200 ps)		
Trigger Types				
Edge	Triggers when signal meets	slope (positive, negative, or ei	ther) and level condition.	
Width			s low as 200ps to 20 s) or on i	ntermittent faults.
Glitch			e as low as 200ps to 20 s) or o	
Window	Triggers when signal exits a	window defined by adjustable	thresholds.	
Pattern	Logic combination (AND, NA high, low or don't care. The h	ND, OR, NOR) of 5 inputs (4 chigh and low level can be selec	annels and external trigger inp ted independently. Triggers at	ut). Each source can be start or end of the pattern.
Runt	Trigger on positive or negative	e runts defined by two voltage	limits and two time limits. Sele	ect between 1 ns and 20 ns.
Slew Rate			elect edge limits between 1 ns	and 20 ns.
Interval	Triggers on intervals selecta			
Dropout		or longer than selected time b		
Exclusion Triggering			ehavior and triggering when th	
Measurement Trigger	be used as only trigger or las	st event in a Cascade Trigger.	rigger on a measurement valu	·
Multi-stage: Qualified	Triggers on any input source sources is selectable by time	e only if a defined state or edge e or events.	e occurred on another input sc	ource. Holdoff between
Multi-stage: Qualified First	In Sequence acquisition mos satisfied in the first segmen	de, triggers repeatably on ever t of the acquisition. Holdoff b	nt B only if a defined pattern, st etween sources is selectable b	ate or edge (event A) is y time or events.
Mult-Stage: Cascade (Sequence) Trigger, Capability	Arm on "A" event, then Trigge	er on "B" event. Or Arm on "A"	event, then Qualify on "B" even	t, and Trigger on "C" event.
Mult-Stage: Cascade (Sequence) Trigger, Types	can be on Stage B only.		litch, Interval, Dropout, or Meas	
inggei, types	Cascade A then B then C (M Measurement. Measurement Cascade A then B then C: Ec	nt can be on Stage C only.	Pattern (Logic), Width, Glitch, In	iterval, Dropout, or
Mult-Stage: Cascade (Sequence) Trigger, Holdoff	Holdoff between A and B or	B and C is selectable by time	or number of events. ade precludes a holdoff setting	between the prior stage
High-speed Serial Protocol Trigg	ering (Optional)			
Data Rates	Option LM10Zi-6GBIT-80B-S Option LM10Zi-14GBIT-80B-	YMBOL-TD: 600 Mb/s to 6.5 (SYMBOL-TD: 600 Mb/s to 14.	1 Gb/s, Channel 4 input only	
Pattern Length	80 bits NRZ, eight 8b/10b sy	lly available on signal rates ≥ 6	J.ZU GD/S)	
i attern Length	oo bits innz, eigiit ob/ 100 S)	//// (1110015, 040/ 000 Syriiddi		

	LabMaster 10-50Zi-A	LabMaster 10-59Zi-A	LabMaster 10-65Zi-A
Triggering System			
Modes	Normal, Auto, Single and Stop		
Sources	Any Ch 1-4 (Edge, Window, SMART, Ca only) on additional 10-xxZi-A Acquisiti Slope and level unique to each source	ascade triggers), AUX, internal Fast Edge on Modules (Channels 5 and higher). except line trigger.	e; or any input channel (Edge trigger
Coupling	DC, AC, HFRej, LFRej		
Pre-trigger Delay	0 to 100% of memory size (adjustable		
Post-trigger Delay	0-10,000 divisions in real time mode		
Hold-off	From 2 ns up to 20 s or from 1 to 99,9	,	
Trigger and Interpolator Jitter	<0.1 ps rms (typical, software assiste	d), 2 ps rms (typical, hardware)	
Internal Trigger Level Range	±4.1 div from center		
External Trigger Level Range	For any LabMaster 10xx-Zi-A Acquisi (Only Ch1-4 Acquisition Module has "	'active'''' AUX Input)	
Maximum Trigger Rate	1,000,000 waveforms/second (in Sec		
Trigger Sensitivity with Edge Trigger (1.85/2.4/2.92mm Inputs)	For Ch 1-80 of a LabMaster 10 Zi-A s 3 div @ < 12 GHz 1.5 div @ < 3 GHz 1.0 div @ < 200 MHz	ystem:	
Trigger Sensitivity with Edge Trigger (Aux Input)	(for DC coupling, ≥ 10 mV/div, 50 Ω)  For Ch 1-4 LabMaster 10xx-Zi-A Acqu 2 div @ < 1 GHz, 1.5 div @ < 500 MHz, 1.0 div @ < 200 MHz, (for DC coupling)	isition Module:	
May Trigger Frequency	For Ch 1-4 only of any LabMaster 10:	wy Zi A Agguinition Module:	
Max. Trigger Frequency, SMART Trigger	2.0 GHz @ ≥ 10 mV/div	xx-Zi-A Acquisition Module:	
	(minimum triggerable width 200 ps)		
Trigger Types			
Edge		sitive, negative or either) and level conc	
Width		widths (widths selectable as low as 20	
Glitch Window	Triggers on positive or negative glitch Triggers when signal exits a window	nes (widths selectable as low as 200 ps defined by adjustable thresholds.	to 20 s) or on intermittent faults.
Pattern		IOR) of 5 inputs (4 channels and externation loss) of 5 inputs (4 channels and externation loss) of 5 inputs (4 channels and externation).	
Runt		efined by two voltage limits and two time	
Slew Rate		r dV, dt and slope. Select edge limits be	tween 1 ns and 20 ns.
nterval	Triggers on intervals selectable betwe		
Dropout		than selected time between 1 ns and 2	
Exclusion Triggering		<u>ifying the expected behavior and trigger</u>	
Measurement Trigger	be used as only trigger or last event in	rement parameters trigger on a measu n a Cascade Trigger.	
Multi-stage: Qualified		defined state or edge occurred on anot ts.	
Multi-stage: Qualified First	satisfied in the first segment of the a	rs repeatably on event B only if a define equisition. Holdoff between sources is	selectable by time or events.
Mult-Stage: Cascade (Sequence) Trigger, Capability		event. Or Arm on "A" event, then Qualify	, 33
Mult-Stage: Cascade (Sequence) Trigger, Types	can be on Stage B only. Cascade A then B then C (Measurem- Measurement. Measurement can be Cascade A then B then C: Edge, Wind	ow, Pattern (Logic)	lth, Glitch, Interval, Dropout, or
Mult-Stage: Cascade (Sequence) Trigger, Holdoff	Holdoff between A and B or B and C i Measurement trigger selection as the and the last stage.	s selectable by time or number of event last stage in a Cascade precludes a ho	ss. Idoff setting between the prior stage
High-speed Serial Protocol Trigg	ering (Optional)		
Data Rates	Option LM10Zi-6GBIT-80B-SYMBOL-7 Option LM10Zi-14GBIT-80B-SYMBOL	TD: 600 Mb/s to 6.5 Gb/s, Channel 4 inp -TD: 600 Mb/s to 14.1 Gb/s, Channel 4 i gnal for triggering when oscilloscope is	input only
Pattern Length	80 bits NRZ, eight 8b/10b symbols, 6		

0000.				
	LabMaster	LabMaster	LabMaster	LabMaster
	10-20Zi-A	10-25Zi-A	10-30Zi-A	10-36Zi-A
Measurement Tools				
Measurement Functionality	deviation, and total number. Histicons provide a fast, dyr addition, subtraction, multip measurement on the source or waveform state.	ent parameters together with Each occurrence of each par- namic view of parameters and lication or division of two diffe waveform. Parameter accep	ameter is measured and adde waveshape characteristics. erent parameters. Parameter t criteria define allowable valu	ed to the statistics table. Parameter math allows gates define the location for ues based on range setting
Measurement Parameters - Horizontal + Jitter	(number of, @level), Fall Tim N Cycle Jitter (peak-peak), N (10-90, @levels), Setup (@le	Cycle, Delay (from trigger, 50 ne (90-10, @levels), Frequency lumber of Points, Period (50% vels), Skew (@levels), Slew Ra @level), A Width (@level), X(va	/ (50%, @level), Half Period (@ , @level), Δ Period (@level), P ate (@levels), Time Interval Er	Dlevel), Hold Time (@level), hase (@level). Rise Time
Measurement Parameters - Vertical	Amplitude, Base, Level@X, N	Maximum, Mean, Median, Mini	mum, Peak-to-Peak, RMS, Sto	d. Deviation, Top
Measurement Parameters - Pulse	Area, Base, Fall Time (90-10 Width (50%)	, 80-20, @levels), Overshoot (p	positive, negative), Rise Time	(10-90, 80-20, @levels), Top,
Measurement Parameters - Statistica (on Histograms)		), Amplitude, Base, Peak@Max Top, X(value)@Peak, Peaks (r		
Math Tools				
Math Functionality	operations on each function	ions traces (F1-F12). The easy trace, and function traces ca	n be chained together to perf	orm math-on-math.
Math Operators - Basic Math	Average (summed), Average Reciprocal, Rescale (with un	e (continuous), Difference (-), hits), Roof, Sum (+)	Envelope, Floor, Invert (negat	e), Product (x), Ratio (/),
Math Operators - Filters		oits vertical), Interpolate (cubi	c, quadratic, sinx/x)	
Math Operators - Frequency Analysis	memory length. Select from	nitude, phase, power density, r Rectangular, VonHann, Hamr	ming, FlatTop and Blackman I	Harris windows.
Math Operators - Functions		two waveforms), Derivative, D , Log (base 10), Reciprocal, Re		
Math Operators - Other	Segment, Sparse	<u> </u>	, , ,	
Measurement and Math Integrati	ion			
	Histograms to display statis to 1 million measurement pa	tical distributions of up to 2 b arameters. Track (display para ogram and persistence trace (	ameter vs. time, time-correlat	
Pass/Fail Testing				
	<, ≤, =, >, ≥, within limit ±∆ va In, or Any Out conditions). Co True", "Any False", or groups	ueries using a Single or Dual F ilue or %) or Mask Test (pre-de ombine queries into a boolear of "All" or "Any", with following screen image, save to clipboa	efined or user-defined mask, v n expression to Pass or Fail IF g THEN Save (waveforms), St	vaveform All In, All Out, Any "All True", "All False", "Any op, Alarm, (send) Pulse,
Display System				
Size	Color 15.3" flat panel TFT-Ac	ctive Matrix LCD with high-reso	olution touch screen	
Resolution	WXGA; 1280 x 768 pixels			
Number of Traces		aces. Simultaneously display		
Grid Styles	some software options.	ad, Octal, X-Y, Single+X-Y, Dua	I+X-Y, I welve, Sixteen. Up to t	wenty grids available with
Waveform Representation	Sample dots joined, or samp	ole dots only		
Processor/CPU				
Туре		-core, 48-thread processor, 2.4	4 GHz per core (up to 4.0 GHz	in Turbo mode) or better
Processor Memory	32 GB standard. Up to 192 (	GB optionally available.		
Operating System	Microsoft Windows® 10			
Oscilloscope Operating Software	Teledyne LeCroy MAUI™ witl			
Real-Time Clock	Date and time displayed with	n waveform in hardcopy files. S	SNIP support to synchronize t	o precision internal clocks.

	LabMaster 10-50Zi-A	LabMaster 10-59Zi-A	LabMaster 10-65Zi-A
Measurement Tools	10 0021 A	10 0321 A	10 0021 A
Measurement Functionality	Display up to 12 measurement parame deviation, and total number. Each occu Histicons provide a fast, dynamic view addition, subtraction, multiplication or measurement on the source waveform or waveform state.	urrence of each parameter is measured of parameters and waveshape charact division of two different parameters. P	d and added to the statistics table. Steristics. Parameter math allows Parameter gates define the location for
Measurement Parameters - Horizontal + Jitter	Cycles (number of), Cycle to Cycle, Del (number of, @level), Fall Time (90-10, ( N Cycle Jitter (peak-peak), Number of (10-90, @levels), Setup (@levels), Skev Time (@level), Width (50%, @level), $\Delta$ V	mevels), Frequency (50%, @lével), Halt Points, Period (50%, @level), Δ Period ( γ (@levels), Slew Rate (@levels), Time Vidth (@level), X(value)@max, X(value)	Feriod (@level), Hold Time (@level), @level), Phase (@level), Rise Time Interval Error (@level), Time (@level), Δ I@min
Measurement Parameters - Vertical	Amplitude, Base, Level@X, Maximum,		
Measurement Parameters - Pulse	Area, Base, Fall Time (90-10, 80-20, @l Width (50%)		
Measurement Parameters - Statistica (on Histograms)	Full Width (@ Half Max, @%), Amplitud Range, RMS, Std. Deviation, Top, X(valu	e, Base, Peak@MaxPopulation, Maximue)@Peak, Peaks (number of), Percent	num, Mean, Median, Minimum, Mode, ile, Population (@bin, total)
Math Tools			
Math Functionality	Display up to 12 math functions traces operations on each function trace, and	function traces can be chained togeth	er to perform math-on-math.
Math Operators - Basic Math	Average (summed), Average (continuou Reciprocal, Rescale (with units), Roof, S	Sum (+)	ert (negate), Product (x), Ratio (/),
Math Operators - Filters	Enhanced resolution (to 15 bits vertica		
Math Operators - Frequency Analysis	FFT (power spectrum, magnitude, phasmemory length. Select from Rectangul	ar, VonHann, Hamming, FlatTop and B	lackman Harris windows.
Math Operators - Functions	Absolute value, Correlation (two waveforms (negate), Log (base e), Log (base		
Measurement and Math Integration	On  Histograms to display statistical distrib to 1 million measurement parameters. parameter. Persistence histogram and	Track (display parameter vs. time, time	e-correlated to acquisitions) any
Pass/Fail Testing			
	Display up to 12 Pass/Fail queries usin $\langle , \leq , = , > , \geq \rangle$ within limit $\pm \Delta$ value or %) o In, or Any Out conditions). Combine queritue", "Any False", or groups of "All" or "Alardcopy (send email, save screen image).	r Mask Test (pre-defined or user-define eries into a boolean expression to Pass Any", with following THEN Save (wavef	ed mask, waveform All In, All Out, Any s or Fail IF "All True", "All False", "Any forms), Stop, Alarm, (send) Pulse,
Display System			
Size	Color 15.3" flat panel TFT-Active Matrix	LCD with high-resolution touch screen	1
Resolution	WXGA; 1280 x 768 pixels	la la	and the second s
Number of Traces Grid Styles	Display a maximum of 40 traces. Simu Auto, Single, Dual, Triple, Quad, Octal, X some software options.	rtaneousiy display channei, zoom, mer -Y, Single+X-Y, Dual+X-Y, Twelve, Sixtee	en. Up to twenty grids available with
Waveform Representation	Sample dots joined, or sample dots onl	у	
Processor/CPU		•	
Type	Intel® Xeon® Gold 6240R 24-core, 48-th	read processor. 2.4 GHz per core (up to	o 4.0 GHz in Turbo mode) or better
Processor Memory	32 GB standard. Up to 192 GB optiona		2 2
Operating System	Microsoft Windows® 10	,	
Oscilloscope Operating Software	Teledyne LeCroy MAUI™ with OneToucl	٦	
Real-Time Clock	Date and time displayed with waveform	in hardcopy files. SNTP support to syn	chronize to precision internal clocks.
	-	•	

	LabMaster 10-20Zi-A	LabMaster 10-25Zi-A	LabMaster 10-30Zi-A	LabMaster 10-36Zi-A			
	IU-ZUZI-A	1U-25ZI-A	10-30ZI-A	10-36ZI-A			
Connectivity							
Ethernet Port		Supports 10/100/1000BaseT Ethernet interface (RJ45 port)					
USB Host Ports	unit to support Windows co	LabMaster MCM-Zi -A Master Control Module: 1 x USB 2.0 ports, 1 x USB Type-C and 4 x USB3.1 Gen1 on rear of unit to support Windows compatible devices LabMaster MCM-Zi -A Master Control Module: minimum 3 total USB 2.0 ports on front of unit to support Windows compatible devices					
GPIB Port (Optional)	Supports IEEE-488.2						
External Monitor Port	Dual Link DVI compatible to resolution) and customer-su desktop mode.	support internal display on M upplied monitor with up to WQ	CM-Zi Master Control Module XGA (2560 x 1600 pixel) resol	(1280 x 768 pixel ution using extended			
Remote Control	Via Windows Automation, o	r via LeCroy Remote Commar	nd Set				
Network Communication Standard	VXI-11 or VICP, LXI Class C (						
Power Requirements							
Voltage	LabMaster 10-xxZi Acquisiti Automatic AC Voltage Selec LabMaster MCM-Zi Master Installation Category II	on Module: 100–240 VAC ±10 tion, Installation Category II Control Module: 100–240 VAC	0% at 45-66 Hz; 100-120 VAC C ±10% at 45-66 Hz; Automation	±10% at 380-420 Hz; c AC Voltage Selection,			
Max Power Consumption	LabMaster 10-xxZi-A Acquis LabMaster MCM-Zi-A Maste Each Module and the CPU	sition Module - 1225 W / 1225 er Control Module - 450 W / 45 nas a separate power cord.	VA. 50 VA.				
Environmental							
Temperature (Operating)	+5 °C to +40 °C						
Temperature (Non-Operating)	-20 °C to +60 °C						
Humidity (Operating)			derating to 50% RH (non-con	densing) at +40 °C			
Humidity (Non-Operating)		sing) as tested per MIL-PRF-28	8800F				
Altitude (Operating)	Up to 10,000 ft (3048 m) at	or below +25 °C					
Altitude (Non-Operating)	Up to 40,000 ft (12,192 m)						
Random Vibration (Operating)		minutes in each of three orth					
Random Vibration (Non-Operating)		minutes in each of three orth					
Functional Shock	20 g peak, half sine, 11 ms p	oulse, 3 shocks (positive and r	negative) in each of three ortho	ogonal axes, 18 shocks tota			
Size and Weight							
Dimensions (HWD)	LabMaster MCM-Zi-A Maste LabMaster 10-xxZi-A Acquis	er Control Module - 10.9""H x 1 sition Module - 8.0""H x 18.2""V	8.2"'W x 15.6"'D (277 x 462 x 3 V x 26""D (202 x 462 x 660 mm	396 mm), n)			
Weight	LabMaster MCM-Zi-A Maste	r Control Module - 47 lbs. (21.4 tion Module -53 lbs. (24.1 kg)					
Shipping Weight	LabMaster MCM-Zi-A Acquisition Module - 36 lbs. (25.5 kg) LabMaster 10-xxZi-A Acquisition Module - 71 lbs. (32.3 kg)						
Certifications							
CE Certification UL and cUL Listing	CE compliant, UL and cUL li CSA C22.2 No. 61010-1-12	sted; conforms to EN 61326, E	EN 61010-1, EN61010-2-030, l	JL 61010-1 3rd edition and			
Warranty and Service							
	3-year warranty calibration	recommended annually Ontic	onal service programs include	extended warranty			

3-year warranty; calibration recommended annually. Optional service programs include extended warranty, upgrades and calibration services.

	LabMaster 10-50Zi-A	LabMaster 10-59Zi-A	LabMaster 10-65Zi-A
Connectivity			
Ethernet Port	Supports 10/100/1000BaseT Ethernet	interface (RJ45 port)	
USB Host Ports	LabMaster MCM-Zi -A Master Control N unit to support Windows compatible de LabMaster MCM-Zi -A Master Control N compatible devices	evices	
GPIB Port (Optional)	Supports IEEE-488.2		
External Monitor Port	Dual Link DVI compatible to support int resolution) and customer-supplied mor desktop mode.	nitor with up to WQXGA (2560 x 1600 p	
Remote Control	Via Windows Automation, or via LeCroy	Remote Command Set	
Network Communication Standard	VXI-11 or VICP, LXI Class C (v1.2) comp	liant	
Power Requirements			
Voltage	LabMaster 10-xxZi Acquisition Module Automatic AC Voltage Selection, Install LabMaster MCM-Zi Master Control Mo Installation Category II	ation Category II	
Max Power Consumption	LabMaster 10-xxZi-A Acquisition Modu LabMaster MCM-Zi-A Master Control N Each Module and the CPU has a separ	1odule - 450 W / 450 VA.	
Environmental			
Temperature (Operating)	+5 °C to +40 °C		
Temperature (Non-Operating)	−20 °C to +60 °C		
Humidity (Operating)	5% to 80% RH (non-condensing) up to -		(non-condensing) at +40 °C
Humidity (Non-Operating)	5% to 95% RH (non-condensing) as tes		
Altitude (Operating)	Up to 10,000 ft (3048 m) at or below +2	25 °C	
Altitude (Non-Operating)	Up to 40,000 ft (12,192 m)		
Random Vibration (Operating)	0.5 grms 5 Hz to 500 Hz, 15 minutes in		
Random Vibration (Non-Operating)	2.4 grms 5 Hz to 500 Hz, 15 minutes in		
Functional Shock	20 g peak, half sine, 11 ms pulse, 3 sho	cks (positive and negative) in each of	three orthogonal axes, 18 shocks total
Size and Weight			
Dimensions (HWD)	LabMaster MCM-Zi-A Master Control N LabMaster 10-xxZi-A Acquisition Modu	1odule - 10.9""H x 18.2""W x 15.6""D (27 le - 8.0""H x 18.2""W x 26""D (202 x 462	77 x 462 x 396 mm), 2 x 660 mm)
Weight	LabMaster MCM-Zi-A Master Control N LabMaster 10-xxZi-A Acquisition Modu	1odule - 47 lbs. (21.4 kg) le -58 lbs. (24.1 kg)	
Shipping Weight	LabMaster MCM-Zi-A Master Control M LabMaster 10-xxZi-A Acquisition Modu	lodule - 56 lbs. (25.5 kg) le -76 lbs. (34.5 kg)	
Certifications			
CE Certification UL and cUL Listing	CE compliant, UL and cUL listed; confo CSA C22.2 No. 61010-1-12	rms to EN 61326, EN 61010-1, EN610	10-2-030, UL 61010-1 3rd edition and
Warranty and Service			
	3-year warranty; calibration recommen upgrades and calibration services.	ded annually. Optional service progran	ns include extended warranty,

### ONDERING IN ORIMATION

### **LabMaster 10 Zi-A Series Master Control Modules**

**Product Description** 

LabMaster Master Control Module with 15.3"	LabMaster MCM-Zi-A
WXGA Color Display.	
SDA Master Control Module with 15.3" WXGA Color	SDA MCM-Zi-A
Display (provides add'l standard software and	
64 Mpt/Ch memory)	

### LabMaster 10 Zi-A Series Acquisition Modules

Labiviaster 10 ZI-A Series Acquisition Modul	es
20 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch	LabMaster 10-20Zi-A
LabMaster 10 Zi Acquisition Module	
with 50 Ω input	
25 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch	LabMaster 10-25Zi-A
LabMaster 10 Zi Acquisition Module	
with 50 Ω input	
30 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch LabMaster	LabMaster 10-30Zi-A
10 Zi Acquisition Module	
with 50 Ω input	
36 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch LabMaster	LabMaster 10-36Zi-A
10 Zi Acquisition Module	
with 50 Ω input	
50 GHz, 160 GS/s, 2 Ch, 64 Mpts/Ch	LabMaster 10-50Zi-A
LabMaster 10 Zi Acquisition Module	
with 50 $\Omega$ input	
(36 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch)	
59 GHz, 160 GS/s, 2 Ch, 64 Mpts/Ch	LabMaster 10-59Zi-A
LabMaster 10 Zi Acquisition Module	
with 50 $\Omega$ input	
(36 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch)	10.057.4
65 GHz, 160 GS/s, 2 Ch, 64 Mpts/Ch	LabMaster 10-65Zi-A
LabMaster 10 Zi Acquisition Module	
with 50 $\Omega$ input	
(36 GHz, 80 GS/s, 4 Ch, 32 Mpts/Ch)	

### Included with LabMaster MCM-Zi-A Standard Configuration

Power Cable for the Destination Country, Optical 3-button Wheel Mouse USB 2.0, Printed Getting Started Manual, Anti-virus Software (Trial Version), Microsoft Windows 7 License, Commercial NIST Traceable Calibration with Certificate, 3-year Warranty

### Included with LabMaster 10-xxZi-A Standard Configuration

2.92mm Connector Saver: Qty. 4, 1.85mm Barrel Adapter: Qty. 2 (50-65 GHz units only), PCIe x 8 cable, 2m long, PCIe x 4 cable, 2m long, Power Cable for the Destination Country, ChannelSync 10 GHz clock cable, 2m long, Commercial NIST Traceable Calibration with Certificate, 3-year Warranty

### ChannelSync Expansion Products

LabMaster CMH20-Zi
LabMaster CMH-1ACQMODULE-Zi

### **Memory Options**

Memory options	
64 Mpts/Ch Memory Option for LabMaster 10 Zi	LM10Zi-M-64
Acquisition Modules	
128 Mpts/Ch Memory Option for LabMaster 10 Zi	LM10Zi-L-128
Acquisition Modules	
128 Mpts/Ch Memory Option for LabMaster 10 Zi	SDA10Zi-L-128
Acquisition Modules. Used with SDA MCM-Zi-A	
256 Mpts/Ch Memory Option for LabMaster 10 Zi	LM10Zi-VL-256
Acquisition Modules	
512 Mpts/Ch Memory Option for LabMaster 10 Zi	LM10Zi-XL-512
Acquisition Modules	
512 Mpts/Ch Memory Option for LabMaster 10 Zi	SDA10Zi-XL-512
Acquisition Modules. Used with SDA MCM-Zi-A	

### **Product Description**

**Product Code** 

CPU, Computer and Other Hardware Options for LabMaster MCM-Zi-A Master Control Module

Additional 500 GB Hard Drive for MCM-Zi-A	MCMZi-500GB-RHD-02
Upgrade to 128 GB RAM for MCM-Zi-A	MCMZI-32-UPG-128GB
Upgrade to 192 GB RAM for MCM-Zi-A	MCMZI-32-UPG-192GB
GPIB Option for LabMaster MCM-Zi-A	GPIB-3

**Product Code** 

### **High-speed Digital Analyzer Systems**

12.5 GS/s High-speed Digital Analyzer with	HDA125-18-SYNC
18ch QuickLink leadset and SYNC connection	
12.5 GS/s High-speed Digital Analyzer with	HDA125-09-SYNC
9ch QuickLink leadset and SYNC connection	
18 channel QuickLink leadset for HDA125	HDA-DLS-18QL
9 channel QuickLink leadset for HDA125	HDA-DLS-09QL

### **Ethernet and DDR Debug Toolkits**

100Base-T1 and 1000Base-T1	LM10Zi-AUTO-ENET-TOOLKIT
Debug Toolkit	
DDR 2/3/4/5 and LPDDR 2/3/4/4X	LM10Zi-DDR5-TOOLKIT
Debug Toolkit	
DDR 2/3/4 and LPDDR 2/3/4/4X Debug Tool	kit LM10Zi-DDR4-TOOLKIT
DDR 2/3 and LPDDR 2/3 Debug Toolkit	LM10Zi-DDR3-T00LKIT
DDR2 and LPDDR2 Debug Toolkit	LM10Zi-DDR2-TOOLKIT

### **Serial Data and Crosstalk Analysis**

SDA Expert single lane eye, noise and jitter analysis for NRZ signals	s LM10Zi-SDAX-NRZ
SDA Expert single lane eye, noise and jitter analysis for PAM3 and PAM4 signals	s LM10Zi-SDAX-PAM
SDA Expert multilane eye, noise and jitter analysis for NRZ, PAM3, PAM4 signals. Includes integrated EyeDrII and VirtualProbe toolkits	LM10Zi-SDAX-COMPLETE
SDA Expert configuration and measurements for NRZ PCI Express signals up to 32 GT/s	LM10Zi-SDAX-PCIE-NRZ
SDA Expert configuration and measurements for USB3.2 signals at 5 Gb/s and 10 Gb/s	LM10Zi-SDAX-USB32
SDA Expert configuration and measurements for USB4 NRZ signals at 10 Gb/s and 20 Gb/s, and PAM3 signals at 40 Gb/s	LM10Zi-SDAX-USB4-TBT
SDA Expert Technology Framework for PCIe 6	LM10ZI-SDAX-PCIE6
Single-Lane Serial Data Analysis Frame-	LM10Zi-SDAIII
work, Eye and Jitter Measurements	
Bundle - Multi-Lane SDA LinQ	LM10Zi-SDAIII-CompleteLinQ
Framework, including Eye, Jitter, Noise,	SDA10Zi-CompleteLinQ
Crosstalk Measurements, with EyeDrII	DDA10Zi-CompleteLinQ
and VirtualProbe	
PCIe 6.0 Transmitter Measurements	LM10Zi-SDAIII-PCIE6
PAMx Serial Data Analysis, Eye, Jitter	LM10Zi-SDAIII-PAMx
and Noise Measurements	

### **Signal Integrity Toolkits**

Advanced De-embedding, Emulation and	LM10Zi-VIRTUALPROBE
Virtual Probing Toolkit	
Signal Integrity Toolkit - Channel & Fixture	LM10Zi-EYEDRII
De-embedding/Emulation, Tx/Rx Equal-	
ization	
Bundle - EyeDrII and VirtualProbe Toolkits	LM10Zi-EYEDRII-VP
Cable De-embed Option	LM10Zi-CBL-DE-EMBED

### **Modulated Signal Analysis**

VectorLinQ Advanced Vector Signal Analysis	LM10Zi-VECTORLINQ-ADV
including OFDM	
VectorLinQ - Flexible Vector Signal Analysis	LM10Zi-VECTORLINQ
for electrical signals (RF and baseband I-Q)	

# ORDERING INFORMATION

### Product Description Product Code Product Description Product Code

CrossSync™ PHY Software	
CrossSync PHY protocol analyzer	LM10ZI-CROSSSYNC-PHY-PCIe
synchronization Option for PCIe	
CrossSync PHY protocol analyzer	LM10ZI-CROSSSYNC-PHY-USB
synchronization Option for USB and	

**Serial Data Compliance** 

Thunderbolt

Serial Data Compliance	
QualiPHY Enabled MultiGBase-T1 (Automotive Ethernet) Compliance Software Option	QPHY-MultiGBase-T1
QualiPHY Enabled 10Base-T1L (Industrial Ethernet) Compliance Software Option	QPHY-10Base-T1L
QualiPHY Enabled 10Base-T1S (Automotive Ethernet) Compliance Software Option	QPHY-10Base-T1S
QualiPHY Enabled 10GBase-KR Software Option	QPHY-10GBase-KR
QualiPHY Enabled 10GBase-T Software Option.	QPHY-10GBase-T
QualiPHY Enabled 56G PAM4 Compliance Software Option	QPHY-56G-PAM4
QualiPHY Enabled LPDDR2 Software Option	QPHY-LPDDR2
QualiPHY Enabled DDR3, DDR3L and LPDDR3 Software Op	tion QPHY-DDR3
QualiPHY Enabled DDR4 and LPDDR4/4X Software Option	QPHY DDR4
QualiPHY DDR5 System Level Software Option	QPHY-DDR5-SYS
DDR5 Bundle Enabled QualiPHY + Debug Toolkit Options for all DDR2/3/4/5 and LPDDR2/3/4/4X	M10Zi-DDR5-BUNDLE
QualiPHY Enabled DisplayPort 2.0 Source Software Option (Includes DP 1.4 Source)	QPHY-DP20-Source
QualiPHY Enabled DisplayPort 1.4 Source Software Option	QPHY-DP14-Source
QualiPHY Enabled DisplayPort 2.0 Sink Software Option	QPHY-DP20-Sink
QualiPHY Enabled Embedded DisplayPort Software Option	QPHY-eDP
QualiPHY Enabled HDMI 2.1 FRL and TMDS Software Optic (Includes HDMI 1.4 and HDMI 2.0)	-
QualiPHY Enabled HDMI 2.0/1.4 FRL TMDS Software Option	n QPHY-HDMI2
QualiPHY Enabled MIPI C-PHY Compliance Software Option (includes C-PHY DMP)	QPHY-MIPI-CPHY
QualiPHY Enabled MIPI M-PHY Compliance Software Option	QPHY-MIPI-MPHY
	QPHY-MIPI-MPHY QPHY-PCIE3-Tx-Rx
Software Option QualiPHY Enabled PCIe 3.0 Transmitter/Receiver	
Software Option QualiPHY Enabled PCIe 3.0 Transmitter/Receiver Compliance Software Option QualiPHY Enabled PCIe 4.0 Transmitter/Receiver	QPHY-PCIE3-Tx-Rx
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver	QPHY-PCIE3-Tx-Rx QPHY-PCIE4-Tx-Rx
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option	QPHY-PCIE3-Tx-Rx QPHY-PCIE4-Tx-Rx QPHY-PCIE5-TX-RX
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle Gen1 Software Option	QPHY-PCIE3-Tx-Rx  QPHY-PCIE5-TX-RX  QPHY-PCIE6-TX-RX
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle Gen1 Software Option	QPHY-PCIE3-Tx-Rx  QPHY-PCIE5-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIE6-TX-RX
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle Gen1 Software Option  QualiPHY Enabled SATA Software Option	QPHY-PCIE3-Tx-Rx  QPHY-PCIE5-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIE6-TX-RX
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle Gen1 Software Option  QualiPHY Enabled SATA Software Option  QualiPHY Enabled SAS-2 Software Option	QPHY-PCIE3-Tx-Rx  QPHY-PCIE5-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIe QPHY-SATA-TSG-RSG QPHY-SAS2
Software Option  QualiPHY Enabled PCle 3.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 4.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 5.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle 6.0 Transmitter/Receiver Compliance Software Option  QualiPHY Enabled PCle Gen1 Software Option  QualiPHY Enabled SATA Software Option  QualiPHY Enabled SAS-2 Software Option  QualiPHY Enabled SAS-3 Software Option	QPHY-PCIE3-Tx-Rx  QPHY-PCIE5-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIE6-TX-RX  QPHY-PCIe QPHY-SATA-TSG-RSG QPHY-SAS2 QPHY-SAS3

 $PCI\ Express,\ SuperSpeed\ USB\ (USB\ 3.0)\ and\ SATA\ Complete\ Hardware/Software\ Test\ Solutions\ are\ available.\ Consult\ Factory.$ 

### **Serial Data Test Fixtures**

USB4 Sideband Test Coupon Fixture (USB Type-C	C) TF-USB-C-SB
USB4 High-speed and Sideband Test Coupon	TF-USB-C-HS
Fixture (USB Type-C)	
HDMI Pull-Up Terminator Quad Pack	TF-HDMI-3.3V-QUADPAK
USB 3.1 (Standard Type A/B Connector) Test Fixtures	TF-USB3
SATA 1.5 Gb/s, 3.0 Gb/s and 6.0 Gb/s	TF-SATA-C-KIT
Compliance Test Fixture Measure Kit	
Test Fixture for 10GBase-T	TF-10GBASE-T
Automotive Ethernet Breakout Test Fixture for 100Ba	ise- TF-AUTO-ENET
T1 and 1000Base-T1 Debug	
4 pack of SMA Connector boards for TF-AUTO-ENET	TF-AUTO-ENET-SMA
MIPI M-PHY input offset adapter dual pack	TF-MIPI-MPHY-DUALPAK

Serial Data Triggers and Decoders	
600 Mb/s to 14.1 Gb/s 80-bit NRZ. I M1	10ZI-14GBIT-80B-SYMBOL-TD
8b/10b and 64b/66b Serial Trigger. Also	
includes 8b/10b and 64b/66b Decode.	4107LCODIT 00D 0\44D0L TD
600 Mb/s to 6.5 Gb/s 80-bit NRZ, LN 8b/10b, 64b/66b Serial Trigger. Also	M10ZI-6GBIT-80B-SYMBOL-TD
includes 8b/10b and 64b/66b Decode.	
64b/66b Decode Annotation Option	LM10Zi-64b66b D
8b/10b Decode Annotation Option	LM10Zi-8B10B D
CAN FD Decode Option	LM10Zi-CAN FDbus D
ENET Decode Option	LM10Zi-ENETbus D
Ethernet 10G Decode Option	LM10Zi-ENET10Gbus D
PCI Express 5.0 to 1.0 Link Layer Decode Option	LM10Zi-PCIEbus D
PCI Express 6.0 to 1.0 Link Layer Decode Option	LM10Zi-PCIE6BUS D
USB4bus Decode, Measure/Graph, and	LM10Zi-USB4bus DME
Eye Measurements Option	
USB4-SB Decode, Measure/Graph, and PHY	LM10Zi-USB4SB DMP
Measurement Option USB4-SB Decode Option	LM10ZI-USB4SB D
USB 3.2 bus D Option (Includes USB2 bus D)	
USB-PD (Power Delivery) Decode, Measure/Grap	h, and LM10Zi-USB 3.2 bus D LM10Zi-USBPD DMP
Physical Layer Option	II, and LIVITUZI-USBPD DIVIP
USB-PD Decode Option	LM10ZI-USBPD D
DP-AUX AUX Decode, Measure/Graph, and Physi	ical LM107i-DPAUX DMP
Layer (Supports all DisplayPort connector types)	
DisplayPort AUX Decode Option	LM10ZI-DPAUX D
USB2-HSIC Decode Option	LM10Zi-USB2-HSICbus D
SATA Decode Annotation Option	LM10Zi-SATAbus D
SAS Decode Annotation Option	LM10Zi-SASbus D
Fibre Channel Decode Annotation Option	LM10Zi-FCbus D
D-PHY Decode Option	LM10Zi-DPHYbus D
DigRF 3G Decode Option	LM10Zi-DigRF3Gbus D
DigRF v4 Decode Option	LM10Zi-DIGRFv4bus D
Audiobus and Decode Option	LM10Zi-Audiobus D
for I <sup>2</sup> S, LJ, RJ, and TDM	L M 1 0 7; A
Audiobus, Decode, and Graph Option for I <sup>2</sup> S, LJ, RJ, and TDM	LM10Zi-Audiobus DG
Manchester Decode Option	LM10Zi-Manchesterbus D
MDIO Decode Option	LM10Zi-MDIObus D
C-PHY (DSI-2/CSI-2) Decode Option	LM10Zi-CPHYBUS D
C-PHY (DSI-2/CSI-2) Decode, Measure/Graph an	
Physical Layer Option	
MIPI D-PHY Decode Annotation Option	LM10Zi-DPHYbus D
MIPI D-PHY Decode and Physical Layer Test Opt	
MIPI M-PHY Decode Annotation Option	LM10Zi-MPHYbus D
MIPI M-PHY Decode Annotation and Physical La	yer LM10Zi-MPHYbus DP
Test Option	
MIPI UniPro Protocol Decode Option	LM10Zi-UNIPRObus D
	Zivirozi orui riobao B
SpaceWire Decode Option	LM10Zi-SpaceWirebus D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option	LM10Zi-SpaceWirebus D LM10Zi-12Cbus D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Op	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Op <sup>1</sup> I <sup>3</sup> C Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Op <sup>1</sup> I <sup>3</sup> C Decode Option SPI Bus and Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10ZI-SPIbus D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Opi I <sup>3</sup> C Decode Option SPI Bus and Decode Option SPMI Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10ZI-SPIbus D LM10Zi-SPMIbus D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Opi I <sup>3</sup> C Decode Option SPI Bus and Decode Option SPMI Decode Option LIN and Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10Zi-SPIbus D LM10Zi-SPMIbus D LM10Zi-LINbus D
SpaceWire Decode Option IPC Bus and Decode Option IPC Decode, Measure/Graph, and Eye Diagram Option IPC Decode Option SPI Bus and Decode Option SPMI Decode Option LIN and Decode Option UART and RS-232 and Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10Zi-SPIbus D LM10Zi-SPMIbus D LM10Zi-LINbus D LM10Zi-UART-RS232bus D
SpaceWire Decode Option I <sup>2</sup> C Bus and Decode Option I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Option I <sup>3</sup> C Decode Option SPI Bus and Decode Option SPMI Decode Option LIN and Decode Option UART and RS-232 and Decode Option FlexRay and Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10ZI-SPIbus D LM10Zi-SPMIbus D LM10Zi-LINbus D LM10Zi-UART-RS232bus D LM10Zi-FlexRaybus D
SpaceWire Decode Option  I <sup>2</sup> C Bus and Decode Option  I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Opi  I <sup>3</sup> C Decode Option  SPI Bus and Decode Option  SPMI Decode Option  LIN and Decode Option  UART and RS-232 and Decode Option  FlexRay and Decode, and	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10Zi-SPIbus D LM10Zi-SPMIbus D LM10Zi-LINbus D LM10Zi-UART-RS232bus D
SpaceWire Decode Option  I <sup>2</sup> C Bus and Decode Option  I <sup>3</sup> C Decode, Measure/Graph, and Eye Diagram Opi  I <sup>3</sup> C Decode Option  SPI Bus and Decode Option  SPMI Decode Option  LIN and Decode Option  UART and RS-232 and Decode Option  FlexRay and Decode Option  FlexRay, Decode, and  Physical Layer Test Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D LM10Zi-I2Cbus D LM10ZI-I3CBUS DME LM10Zi-SPIBUS D LM10Zi-SPMIbus D LM10Zi-LINbus D LM10Zi-UART-RS232bus D LM10Zi-FlexRaybus DP
SpaceWire Decode Option  1ºC Bus and Decode Option  1ºC Decode, Measure/Graph, and Eye Diagram Opi  1ºC Decode Option  SPI Bus and Decode Option  SPMI Decode Option  LIN and Decode Option  UART and RS-232 and Decode Option  FlexRay and Decode Option  FlexRay, Decode, and Physical Layer Test Option  MIL-STD-1553 Decode Option	LM10Zi-SpaceWirebus D LM10Zi-I2Cbus D tion LM10ZI-I3CBUS DME LM10ZI-I3CBUS D LM10ZI-SPIbus D LM10Zi-SPMIbus D LM10Zi-LINbus D LM10Zi-UART-RS232bus D LM10Zi-FlexRaybus D

Decode Annotation and Protocol Analyzer

SENT Decode Option

Synchronization Software Option

Decode Annotation and Protocol Analyzer
Synchronization Software + Bit Tracer Option

LM10Zi-ProtoSync

LM10Zi-SENTbus D

LM10Zi-ProtoSync-BT

# ORDERING INFORMATION

### Product Description

### **Product Code**

### Product Description

### **Product Code**

### **General Purpose and Application Specific Software Options**

Spectrum Analyzer for LabMaster 10 Zi - 1 trace	LM10Zi-SPECTRUM-1
Spectrum Analyzer for LabMaster 10 Zi -	LM10Zi-SPECTRUM-PRO-2R
2 traces + reference	
MAUI Studio Pro Offline Remote and PC	MAUI Studio Pro
Analysis Software License	
Digital Filter Software Package	LM10Zi-DFP2
Serial Data Mask Software Package	LM10Zi-SDM
Disk Drive Measurements Software Package	LM10Zi-DDM2
Disk Drive Analyzer Software Package	LM10Zi-DDA
Advanced Optical Recording Measurement Pack	age LM10Zi-AORM
EMC Pulse Parameter Software Package	LM10Zi-EMC
Clock Jitter Analysis with Four Views Software Package	LM10Zi-JITKIT
Continuit i donage	

### Miscellaneous

MCM-Zi-A Rackmount Kit	MCM-Zi-RACKMOUNT
LabMaster 10 Zi-A Acquisition Module	LM10Zi-ACQMOD-RACKMOUNT
Rackmount Kit	

Probes and Probe Accessories	
25 GHz differential probe with 2.92mm interface	DH25-2.92MM
30 GHz differential probe with 2.92mm interface	DH30-2.92MM
DH series high-sensitivity solder-in tip, 30 GHz BW, 2.0 Vpl range	p DH-SI-HS
DH series solder-in tip, 30 GHz BW, 3.5 Vpp range	DH-SI
DH series high-temperature solder-in tip, 16 GHz BW, 3.5 $\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	/pp DH-HITEMP
DH series QuickLink adapter, 8 GHz BW	DH-QL
DH series QuickLink adapter kit with 3 x QL-SI tips	DH-QL-3SI
DH series PT browser tip, 16 GHz BW, 3.5 Vpp range	DH-PT
250 MHz 60 V Common Mode Differential Probe	DL02-HCM
500 MHz 60 V Common Mode Differential Probe	DL05-HCM
1 GHz 60 V Common Mode Differential Probe	DL10-HCM
WaveLink 13 GHz, 2.0 Vp-p Differential Probe System	D1305-A-PS
WaveLink 16 GHz, 2.0 Vp-p Differential Probe System	D1605-A-PS
WaveLink 20 GHz, 2.0 Vp-p Differential Probe System	D2005-A-PS
WaveLink 25 GHz, 2.0 Vp-p Differential Probe System	D2505-A-PS
Power/Voltage Rail Probe 2 GHz, 1.2x, ±60V offset, ±800mV dynamic range	RP2060
Power/Voltage Rail Probe 4 GHz, 1.2x, ±30V offset, ±800mV dynamic range	RP4060
RP2060/RP4060 Browser Tip accessory. Includes 0 Ohm (1x), 450 Ohm (10x) and 950 Ohm (20x) tips.	RP4000-BROWSER
	RP4000-MCX-LEAD-SI
2.92mm to ProLink Adapter with probe power and communications pass through	L2.92A-PLINK
2.92mm to ProBus Adapter with probe power and communications pass through	L2.92A-PBUS

### **Customer Service**

Teledyne LeCroy oscilloscopes and probes are designed, built and tested to ensure high reliability. In the unlikely event you experience difficulties, our digital oscilloscopes are fully warranted for three years and our probes are warranted for one year. This warranty includes:

- No charge for return shipping
- Long-term 7-year support
- Upgrade to latest software at no charge



1-800-5-LeCroy teledynelecroy.com

Local sales offices are located throughout the world. Visit our website to find the most convenient location.

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